

Instruction Manual Universal Fieldbus-Gateway UNIGATE[®] MB - MPI



Deutschmann Automation GmbH & Co. KG www.deutschmann.com | wiki.deutschmann.de

Manual Art.-No.: V4046E

1	Information on CE marking of the module	. 8
	1.1 EU Directive EMC	
	1.2 Scope of application	
	1.3 Note installation guidelines	
	1.4 Installation of the unit 1.5 Working on switch cabinets 1.5	
•		
2	Information for the machine manufacturers	
	2.1 Introduction	
~	2.2 EU Machinery Directive	
3		
	3.1 UNIGATE® MB software flow-chart	
	 3.2 UNIGATE[®] block diagram	
4	Operation modes of the Gateway	
	4.1 Configuration mode (config mode)	
	4.2 Data exchange mode	
5	RS-interface	
5	5.1 RS-interfaces at the UNIGATE [®] MB	
	5.2 Buffer sizes at the UNIGATE® MB	
	5.3 Framing Check	
6	SSI-interface	
U	6.1 Initiation of the SSI-interface	
	6.1.1 Parameter sample frequency (Clock stretch)	
	6.1.2 Parameter Encoder monitoring (Check Encoder)	
	6.2 Hardware-wiring	
7	Mode of operation of the system	
	7.1 General explanation	
	7.2 Interfaces	
	7.3 Data exchange	
	7.4 Possible data lengths	
	7.5 Startup phase	18
8	Implemented protocols in UNIGATE [®] MB	19
	8.1 Protocol: Transparent	
	8.1.1 Data structure	19
	8.2 Protocol: Universal 232	
	8.2.1 Data structure	
	8.2.2 Fieldbus parameters	
	8.2.3 RS232 parameter table	
	8.2.3.1 Length 232 (232 Length)	
	8.2.3.3 Timeout	
	8.2.3.4 End character (232 End character)	
	8.2.4 Communication sequence	
	8.3 Protocol: 3964(R)	

8.3.1 Data structure 3964R	21
8.3.2 Protocol definitions	21
8.3.3 Data communication	21
8.3.3.1 Initiation of data communication by the low-priority user	
8.3.3.2 Conflicts	
8.3.3.3 Timeout times	
8.3.3.4 Retries	
8.3.3.5 Initiation of data communication by the high-priority user	
8.3.4 Protocol type 3964	22
8.4 Protocol: MODBUS-RTU	
8.4.1 Notes	
8.4.2 UNIGATE [®] as MODBUS-Master	
8.4.2.1 Preparation	
8.4.2.2 Data structure	
8.4.2.3 Communication sequence	
8.4.3 UNIGATE [®] as MODBUS-Slave	23
8.4.3.1 Preparation	23
8.4.3.2 Data structure	24
8.4.3.3 Communication sequence	
8.4.4 UNIGATE [®] as Modbus-ASCII Master	24
8.5 Protocol SSI	24
8.6 The trigger byte	25
8.7 The length byte	25
8.8 Protocol "Universal Modbus RTU Slave"	25
8.8.1 Data structure on the fieldbus side e.g.: PROFIBUS	26
8.8.1.1 Example: FC1 + FC2	26
8.8.1.2 Example: FC3 (Read Holding Register) + FC4 (Read Input Register)	27
8.8.1.3 Example: Write Single Coil FC5	28
8.8.1.4 Example: Write Single Register FC6	29
8.8.1.5 Example: Force multiple coils FC 15	30
8.8.1.6 Example: Preset multiple register FC16	30
8.9 Protocol "Universal Modbus RTU Master"	31
8.9.1 Data structure Fieldbus side (e.g. PROFIBUS):	31
8.9.2 Data structure Application side:	
8.9.3 Configuration: via Wingate since wcf Datei Version 396	32
8.9.3.1 Example: Read coil status FC1	33
8.9.3.2 Example: Read input status FC2	34
8.9.3.3 Example: Read multiple register FC3	35
8.9.3.4 Example: Read input registers FC4	36
8.9.3.5 Example: Force single coil FC5	36
8.9.3.6 Example: Preset single register FC6	37
8.9.3.7 Example: Force multiple coils FC15	37
8.9.3.8 Example: Preset multiple register FC16	38
8.10 Protocol "Universal Modbus ASCII Master/Slave"	39
8.10.1 Appendix	
8.10.1.1 Example Configuration 1:	39
8.10.1.2 Swap Word	41
8.10.1.3 Example with Fast Ethernet	43

9	Hardware ports, switches and LEDs	. 45			
	9.1 Device labeling				
	9.2 Connectors				
	9.2.1 Connector to the external device (RS-interface)				
	9.2.2 Connector supply voltage				
	9.2.3 MPI-bus-connector				
	9.2.4 Power supply				
	9.3 LEDs				
	9.3.1 LED "(Bus) Power"				
	9.3.2 LED "Bus"				
	9.3.3 LED "(Bus) State"				
	9.3.4 LED "Power"				
	9.3.6 LED State				
	9.4 Switches				
	9.4.1 Termination Rx 422 + Tx 422 (serial interface)				
	9.4.2 Rotary coding switches S4 + S5 (serial interface)				
	9.4.3 Termination (MPI)				
	9.4.4 Rotary coding switches High + Low (MPI-bus-ID)				
10	Error handling				
	10.1 Error handling at UNIGATE [®] MB				
11	Installation guidelines				
••	11.1 Installation of the module				
	11.1.1 Mounting				
	11.1.2 Removal				
	11.2 Wiring				
	11.2.1 Connection systems				
	11.2.1.1 Power supply				
	11.2.1.2 Equipotential bonding connection	52			
	11.2.2 MPI-bus communication interface				
	11.2.2.1 Bus line with copper cable				
	11.2.3 Line routing, shield and measures to combat interference voltage				
	11.2.4 General information on line routing				
	11.2.4.1 Shielding of lines	53			
12	Representation of the data in a S7	. 54			
	12.1 Data exchange	54			
13	Technical data	. 55			
	13.1 Device data	55			
	13.1.1 Interface data	56			
14	Commissioning guide	. 57			
	14.1 Note				
	14.2 Components				
	14.3 Installation				
	14.4 Dimensional drawing UNIGATE [®] MB - MPI				
	14.5 Commissioning				
	14.6 Setting the MPI-address	58			

	14.7	MPI-bus-connection
	14.8	Connection to the process device
		Connecting the supply voltage
	14.10	Shield connection
	14.11	Literature
15	Serv	icing
	15.1	Returning a device
	15.2	Downloading PC software
16	Anne	ΞΧ
	16.1	Explanations of the abbreviations
	16.2	Hexadecimal table

Disclaimer of liability

We have checked the contents of the document for conformity with the hardware and software described. Nevertheless, we are unable to preclude the possibility of deviations so that we are unable to assume warranty for full compliance. The information given in the publication is, however, reviewed regularly. Necessary amendments are incorporated in the following editions. We would be pleased to receive any improvement proposals which you may have.

Copyright

Copyright (C) Deutschmann Automation GmbH & Co. KG 1997 – 2021. All rights reserved. This document may not be passed on nor duplicated, nor may its contents be used or disclosed unless expressly permitted. Violations of this clause will necessarily lead to compensation in damages. All rights reserved, in particular rights of granting of patents or registration of utility-model patents.

1 Information on CE marking of the module

1.1 EU Directive EMC

The following applies to the module described in this User Manual:

Products which bear the CE mark comply with the requirements of EU Directive "Electromagnetic Compatibility" and the harmonized European Standards (EN) listed therein.

The EU Declarations of Conformity are available at the following location for perusal by the responsible authorities in accordance with the EU Directive, Article 10:

Deutschmann Automation GmbH & Co. KG, Carl-Zeiss-Straße 8, 65520 Bad Camberg, Germany.

1.2 Scope of application

The modules are designed for use in the industrial sector and comply with the following requirements.

Scope of application	Requirement applicable to	
	Emitted interference	
Industry	EN 55011, cl. A (2007)	EN 61000-6-2 (2005)

1.3 Note installation guidelines

The module complies with the requirements if you

- 1. comply with the installation guidelines described in the User Manual when installing and operating the module.
- 2. also follow the rules below on installation of the equipment and on working on switch cabinets.

1.4 Installation of the unit

Modules must be installed in electrical equipment rooms/areas or in enclosed housings (e.g. switch boxes made of metal or plastic). Moreover, you must earth the unit and the switch box (metal box) or at least the top-hat rail (plastic box) onto which the module has been snapped.

1.5 Working on switch cabinets

In order to protect the modules against static electrical discharge, the personnel must discharge themselves electrostatically before opening switch cabinets or switch boxes.

2 Information for the machine manufacturers

2.1 Introduction

The UNIGATE[®] module does not constitute a machine as defined by the EU "Machinery" Directive. Consequently, the module does not have a Declaration of Conformity in relation to the EU Machinery Directive.

2.2 EU Machinery Directive

The EU Machinery Directive stipulates the requirements applicable to a machine. The term "machine" is taken to mean a totality of connected parts or fixtures (see also EN 292-1, Paragraph 3.1)

The module is a part of the electrical equipment of the machine and must thus be included by the machine manufacturer in the Declaration of Conformity process.

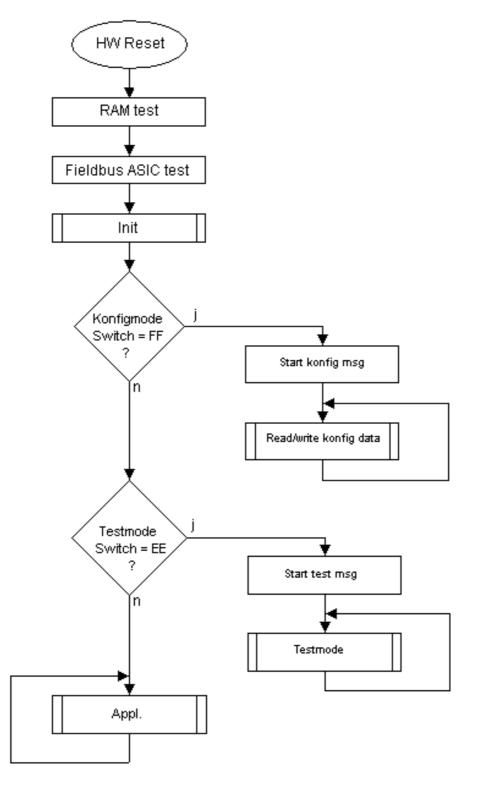
3 Introduction

The module UNIGATE[®] MB-MPI serves to adapt a serial port to the Siemens MPI-bus. In this application, it functions as a Gateway and operates as MPI master or slave, optionally adjustable via Script. It is in the position to communicate with any MPI-device (e. g. S7-300).

The module MB-MPI essentially consists of the following hardware components:

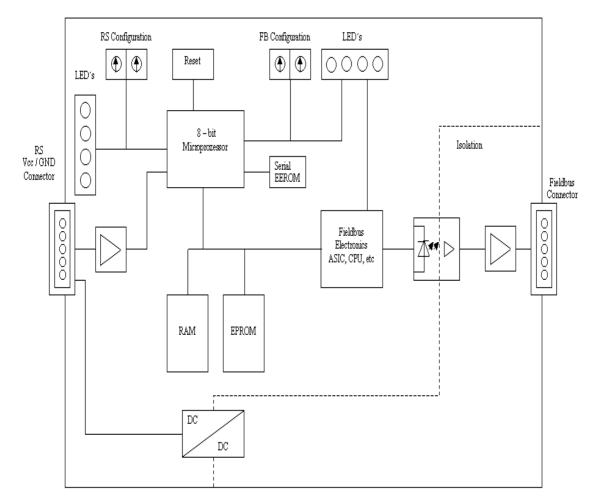
- Electrically isolated RS485 interface to the MPI-bus
- ASIC "MPI 12x"
- Microprocessor 89C51RD2
- RAM and EPROM
- Serial interface (RS232, RS485 and RS422) to the device connected externally

3.1 UNIGATE[®] MB software flow-chart



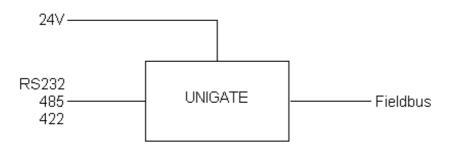
3.2 UNIGATE[®] block diagram

The following picture shows a typical UNIGATE[®]-module design.



3.3 UNIGATE[®] application diagram

The following graph shows a typical connection scheme.



4 Operation modes of the Gateway

4.1 Configuration mode (config mode)

The configuration mode serves to configure the Gateway. The following adjustments are possible in this mode.

- Loading a Script
- Updating the firmware
- Configuring the Gateway

The Gateway will be starting in this mode in case both switches S4 as well as S5 are set on position "F" when switching on the Gateway. Right after switching on the Gateway in the configuration mode it will be sending its starting message, that looks analog with the following message:

```
"RS-MPI-CL-MB (232/422/485) PV3.2[40] (c)dA[40MHz] Switch=0x01FF
Script(C:11634/16128,V:8142/8192)="Universalscript Deutschmann" Author="G/S"
Version="V 1.3" Date=06.08.2014 SN=47110001".
```

In the configuration mode the Gateway always operates with the settings 9600 Bauds, no Parity, 8 Databits and 1 Stopbit, the RS-State LED will always be flashing red, the "Error No/Select ID" LEDs are of no account for the user. All software revisions contain the configuration mode.

4.2 Test mode

Setting of the test mode

The test mode is set by bringing the switches S4 and S5 in position "E". All other switches will not be taken into consideration for the setting of the test mode. Now the Gateway has to be restarted with these settings (by a short disconnection from the power supply).

In the test mode the Gateway always operates with the settings 9600 baud, no parity, 8 databits and 1 stopbit.

The test mode may be helpful to integrate the Gateway in the relevant environment, for instance to test the parameters of the RS-interfaces.

In the test mode the following default is valid on the Fieldbus side:

MPI Partner Addr = 2 MPI Write Type = 0x44; // DB MPI Write Type-No = 1; // DB 1 MPI Write Address = 0; // DW 0 MPI Read Type = 0x44; // DB MPI Read Type-No = 1; // DB 1 MPI Read Address = 0; // DW 0

Mode of operation of the test mode

After the restart in the test mode the Gateway will be sending the values 0-15 in hexadecimal representation ("0"..."F") in ASCII-coding on the serial side every second. Simultaneously the same values are issued binary on the fieldbus-interface.

In this mode the State-LED on the RS-side will be flashing red, the "Error No/Select ID" LEDs will be displaying the value in a binary way, that is issued that moment. Additionally each character that is received at one of the interfaces will also be output at the same interface as a local echo. On the fieldbus-side only the first byte will be used for the local echo, that means on receiving as well as on transmitting only the first byte of the bus data is looked at, the other bus data do not change compared to the last data.

4.3 Data exchange mode

The Gateway has to be in the data exchange mode, so that a data exchange between the RS-side of the Gateway and the fieldbus is possible. As long as the Gateway is not in the configuration mode or the test mode, the data exchange mode is active. In the data exchange mode the Gateway will execute the downloaded Script.

5 RS-interface

5.1 RS-interfaces at the UNIGATE[®] MB

The UNIGATE[®] MB - MPI has the interfaces RS232, RS422 and RS485 available.

5.2 Buffer sizes at the UNIGATE[®] MB

 ${\sf UNIGATE}^{\circledast}$ MB features at the serial side a buffer with the size of 1024 bytes for input data and output data each.

5.3 Framing Check

The length of the stop bit received by the Gateway is checked through the function "Framing Check". Here the stop bit generated by the Gateway is always long enough, so that connected participants can evaluate the stop bit.

Please be aware that the function "Framing Check" becomes effective only in case of 8 data bit and the setting "No parity".

An error is detected and indicated by the Error LEDs in case the stop bit does not show the length 1 bit during the activated check.

The fixed setting for the "Stop Bit Framing Check" is "enabled".

6 SSI-interface

The UNIGATE[®] also supports the connection of applications or products, that communicate via SSI.

6.1 Initiation of the SSI-interface

The configuration of the SSI-interface is executed in the config mode with the WINGATE software, Protocol SSI. The encoder type and the sampling frequency are defined via the parameter "Resolution" (1 bit..15 bit, 24 bit...25 bit), "SSI Encoder Type" (Binary or Gray code) and "Clock stretch".

6.1.1 Parameter sample frequency (Clock stretch)

You can change the sampling frequency. For this purpose a "Stretch value" is passed that inserts a waiting period after each clock edge.

If a 0 is passed, there is no waiting time.

Thus the following SSI sample frequencies may vary slightly:

Waiting time = 0	\rightarrow SSI-Clock ~ 333kHz (No Stretch)
Waiting time = 1	\rightarrow SSI-Clock ~ 185kHz
Waiting time = 2	\rightarrow SSI-Clock ~ 150kHz
Waiting time = 3	\rightarrow SSI-Clock ~ 125kHz
Waiting time = 4	\rightarrow SSI-Clock ~ 110kHz
Waiting time = 5	\rightarrow SSI-Clock ~ 100kHz
Waiting time = 6	\rightarrow SSI-Clock ~ 88kHz
Waiting time = 7	\rightarrow SSI-Clock ~ 80kHz
Waiting time = 8	\rightarrow SSI-Clock ~ 72kHz
Waiting time = 9	\rightarrow SSI-Clock ~ 67kHz
Waiting time = A	\rightarrow SSI-Clock ~ 62kHz
Waiting time = B	\rightarrow SSI-Clock ~ 58kHz
Waiting time = C	\rightarrow SSI-Clock ~ 54kHz
Waiting time = D	\rightarrow SSI-Clock ~ 50kHz
Waiting time = E	\rightarrow SSI-Clock ~ 48kHz
Waiting time = F	\rightarrow SSI-Clock ~ 45kHz

The bit time from which these frequencies were derived, calculate as follows: t = $3\mu s + (2^* (+ 0.6\mu s (n^* 0.6\mu s)))$, where n corresponds to the "Stretch value" (1.. F). Without clock extension (n = 0) remains at $3\mu s \rightarrow 333$ kHz! The max. Bit length of 32 bits and the slowest clock this results in a total readout time of $32^* = 22\mu s \sim 700\mu s$.

6.1.2 Parameter Encoder monitoring (Check Encoder)

An encoder monitoring can be activated via the parameter "Check encoder", as long as the used SSI-encoder supports this function. After the last read encoder bit it is verified if the data line is still at Low for at least one bit. If the UNIGATE[®] does NOT detect this bit on Low, error 12 is issued. For example it can detect a cable break or a not connected encoder. However, it can also be a misconfigured bit length, or a too slow read out clock.

6.2 Hardware-wiring

The clock wires of the SSI-interface are placed onto the Tx-wires of the RS422-interface and the data wires onto the Rx-wires at the UNIGATE[®] MB.

X1 (3pin + 4pin screw-plug-connector):

Pin no.	Name	Function at SSI
1	Rx 232	n. c.
2	Tx 232	n. c.
3	AP-GND	n. c.
4	Rx 422+	SSI DAT+
5	Rx 422-	SSI DAT-
6	Tx 422+	SSI CLK+
7	Tx 422-	SSI CLK-

7 Mode of operation of the system

7.1 General explanation

Communication can be split into seven layers, Layer 1 to Layer 7, in accordance with the ISO/OSI model.

The Deutschmann Automation Gateways convert Layers 1 and 2 of the customized bus system (RS485 / RS232 / RS422) to the corresponding Fieldbus system. Layers 3 to 6 are blank, and Layer 7 is converted in accordance with chapter 7.3.

The Gateway can be configured via the software WINGATE.

7.2 Interfaces

The Gateway features the RS232-, RS422- and RS485-interfaces. The switching of the interfaces is done via the configuration (Parameter 232 Interface).

7.3 Data exchange

All data is transferred by the Gateway in dependence of the configuration.

7.4 Possible data lengths

The table below shows the maximum transferable Fieldbus data:

Reading	255 bytes	Maximum data length
Writing	255 bytes	Maximum data length

Supported are:

Reading- and writing-accesses on DB, markers, inputs and outputs as well as passive accesses on markers and DB.

7.5 Startup phase

In the run-up phase the Gateway programs and configures itself with the partner station at the bus. Data exchange with the external device does not occur until after the run-up phase has been completed with no errors.

8 Implemented protocols in UNIGATE® MB

UNIGATE[®] MB is supplied with the Script "Universal Script Deutschmann". The configuration of the protocols is carried out in the configuration mode (see Chapter 4.1) with the software WING-ATE. See "Instructions UNIGATE[®] CL - Configuration with WINGATE". The PDF can also be found on our website under Support/Downloads/Manuals.



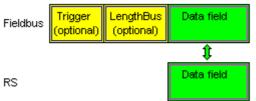
Attention: If a Reset Device is carried out it is possible (depending on the firmware version of the UNIGATE) that the "Universal Script" will get lost and must be played in again. If you no longer have the compiled script, a corresponding request must be sent to Deutschmann Support.

https://www.deutschmann.de/en/support/enquiry/

8.1 Protocol: Transparent

The data is transferred bidirectional from the UNIGATE[®].

8.1.1 Data structure



On the RS-entry side the timeout time of 2 ms is firmly set. If no more data is received within the timeout period, then the data that has been received so far is transferred to the bus.

If less data is received through Rx then configured by the device description file (I/O-length), then the rest is complemented with ZERO.

Too much data received will be cut off.

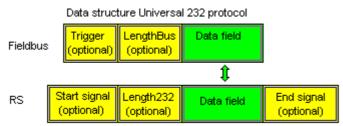
Depending on the fieldbus, the required length of the input and output data (I / O length) can be set via the device configuration of the UNIGATE or via the device description file in the higher-level controller.

8.2 Protocol: Universal 232



The protocol designation "Universal 232" and the relation to the "RS232-interface" in the description have eveloped over the years. The protocol also works with RS422 and RS485 though!

8.2.1 Data structure



8.2.2 Fieldbus parameters

Trigger byte: See "The trigger byte", chapter 8.6 Length byte: See "The length byte", chapter 8.7

8.2.3 RS232 parameter table

8.2.3.1 Start character (232 Start character)

If this character is defined, the gateway evaluates only the data at the RS232 interface following this start character. Each transmission from the gateway via the RS232 interface is initiated with the start character in this case.

8.2.3.2 Length 232 (232 Length)

Attention:

If this byte is activated, the gateway, at the receive end, awaits as many bytes of useful data as specified in this byte by the RS232 transmitter. At the transmission end, the gateway then sets this byte to the number of useful data items transmitted by it. If byte "Length232" is not defined, the gateway, on reception at the RS232 interface, waits for the end criterion if this is defined. If no end criterion is defined either, as many characters as can be transferred in the fieldbus transmit buffer are read in via the RS232 interface.

As a special case for this parameter also a length byte with additional Timeout monitoring can be set in WINGATE. In that case the received characters will be discarded at a Timeout.



If "Timeout" is selected as end character, then this byte has no significance.

8.2.3.3 Timeout

If the end character is set to "FF", the value that was set in the RX_Timeout parameter is activated and the time entered there is waited for with serial reception, or triggered for new characters. If the set time is exceeded without an event, the end criterion is reached and the characters are copied onto the bus.

8.2.3.4 End character (232 End character)

If this character is defined, the gateway receives data from the RS232 interface up to this character. The "Timeout" criterion can be defined as a special case. In this case, the gateway continues to receive characters until a defined pause occurs. In the special case "Timeout" the "Length 232-byte" has no significance. At the transmit end, the gateway inserts the end character, if defined, as the last character of a transmission.

8.2.4 Communication sequence

The useful data (data area) arriving via the fieldbus is copied in accordance with chapter 8.2.1 transparently into the RS232 data field and transferred via the RS interface, whereby the protocol is supplemented in accordance with the configuration (start character, end character...). <u>NO acknowledgement is issued !</u>

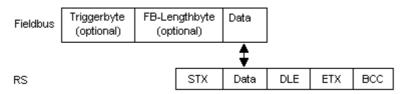
If the "Trigger byte" (see chapter 8.6) is active, data is sent only on a change of this byte. If the "Length byte" (see chapter 8.7) is active, only as many of the following bytes as specified there are transferred.

Receive data at the RS interface is evaluated in accordance with the configured protocol, and the data field (data area (see chapter 8.2.1)) is sent to the fieldbus Master. If more characters have been received than the fieldbus block length, the trailing bytes are truncated and an Rx Overrun is indicated. If less have been received, padding with 0 occurs. If the "Length byte" is active, the number of received useful data items is entered there. If the, "Trigger byte" is active, this is incremented by one after each complete reception operation at the RS interface.

8.3 **Protocol: 3964(R)**

The 3964 protocol is used to transfer data between two serial devices. One partner must be a high-priority partner and the other must be a low-priority partner in order to resolve initialisation conflicts.

8.3.1 Data structure 3964R



8.3.2 Protocol definitions

The telegram format is as follows:

STX Data DLE ETX BCC

• The received net data is forwarded (transparently) in both directions unchanged.

Attention: The DLE-doubling is excluded from it; that means one DLE (10H) on the bus-side is sent on the RS-side twice. A double DLE on the RS-side is only sent once to the bus-master.
Data blocking is not scheduled.

- The net data length is restricted to 236 bytes per telegram.
- Communication always runs between high-priority and low-priority communication partners.

8.3.3 Data communication

8.3.3.1 Initiation of data communication by the low-priority user

If the low-priority user also receives an STX in response to a transmitted STX, it interrupts its transmit request, reverts to Receive mode and acknowledges the received STX with DLE.

A DLE in the data string is duplicated and included in the checksum. The BCC is computed from XORing all characters.

8.3.3.2 Conflicts

8.3.3.3 Timeout times

The timeout times are preset by the definition of the 3964R protocol and cannot be overwritten !!! tq = acknowledgement timeout time (2 s).

The acknowledgement timeout time is started after transmission of control character STX. If no positive acknowledgement arrives within the acknowledgement timeout time, the job is repeated (max. 2 x). If it has not been possible to complete the job positively after two repetitions, the high-priority device nevertheless attempts to establish contact with the low-priority partner by transmitting STX (cycle corresponds to tq).

tz = character timeout time (200 ms)

If the 3964 R driver receives data, it monitors arrival of the individual characters within period tz. If no character is received within the timeout time, the protocol terminates transfer. No acknowledgement is sent to the coupling partner.

8.3.3.4 Retries

In the event of negative acknowledgement or timeout, a telegram transmitted by the high-priority user is repeated twice. After this, the gateway signals communication as disturbed but still attempts to re-establish the connection.

8.3.3.5 Initiation of data communication by the high-priority user

In the case of a negative acknowledgement or timeout, a telegram transmitted by the external device is repeated twice before a fault is signalled.

8.3.4 Protocol type 3964

The difference to protocol type 3964R is:

- 1. tq = acknowledge monitoring time
- 2. The checksum byte BCC is missing.

8.4 Protocol: MODBUS-RTU

8.4.1 Notes

- \rightarrow For reasons of simplicity, "MODBUS-RTU" is referred to as "MODBUS" in the text below.
- \rightarrow The terms "input" and "output" are always viewed from the gateway's point of view, i.e. fieldbus input data is the data sent by the fieldbus Master to the gateway.

8.4.2 UNIGATE® as MODBUS-Master

8.4.2.1 Preparation

Before data exchange is commenced, the parameters "Baud rate", "Parity", "Start bits", "Stop bits" and "Data bits" and, if applicable, the "Trigger byte" and the "Length byte" must be set.

In addition, a "Response time" which corresponds to the maximum time up to which the Modbus Slave responds after a request must be set. UNIGATE[®] multiplies the value entered in WINGATE by 10 ms.

Since the Modbus operates with a variable data format - dependent on the required function and data length - but since the fieldbus requires a fixed data length, this must be preset by means of a selection in the device description file (input and output are identical). This length should be selected by the user such that the longest Modbus request resp. response can be processed.

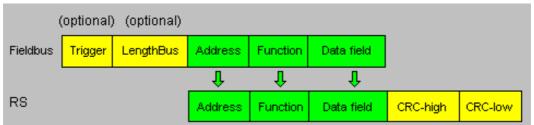
The user can choose whether the fieldbus requests are forwarded to the Modbus in case of a change (On Change) or on request (On Trigger).

In "Change" mode, detection of a change is based on the fact that the fieldbus data is compared with that of the last transmission, and a request is issued by the Modbus only in the case of a change.

The mode "Modbus request on demand" necessitates the first byte in the fieldbus containing a trigger byte (see chapter 8.6). This byte is not transferred to the Modbus and serves only to start a Modbus transmission. For this purpose, the gateway constantly monitors this trigger byte and sends data to the Modbus only when this byte has changed. In the reverse direction (to the fieldbus), the gateway transfers the number of received Modbus data records in this byte, i.e. this byte is incremented by the gateway after each data record.

If the "Length byte" is activated (see chapter 8.7), the gateway transfers only the number of bytes specified there. The number of received Modbus data items is saved in the direction of the fieldbus Master. The length always refers to bytes "Address" to "Dat n" (inclusive in each case), always without CRC checksum.

8.4.2.2 Data structure



8.4.2.3 Communication sequence

The gateway always acts as the Slave with respect to the fieldbus and always acts as the Master at the Modbus end. Thus, data exchange must always be started by the fieldbus Master. The gateway fetches this data which must be structured in accordance with chapter "Data structure", from the fieldbus Master, determines the valid length of the Modbus data if the length byte is not activated, adds the CRC checksum and sends this data record as a request on the Modbus.

The response of the selected Slave is then sent to the fieldbus Master by the gateway - without CRC checksum. If no response occurs within the stipulated "Response time", the gateway signals a "TIMEOUT ERROR".

8.4.3 UNIGATE[®] as MODBUS-Slave

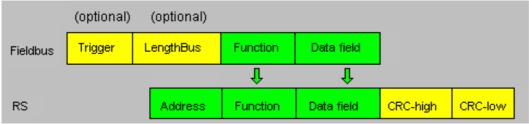
8.4.3.1 Preparation

Before data exchange is commenced, the parameters "Trigger byte" and "Length byte", "Baud rate", "Parity", "Start bits", "Stop bits" and "Data bits" must be set.

At the rotary switch on the RS-side the MODBUS-ID has to be set, under which the gateway is addressed in the Modbus.

Since the Modbus operates with a variable data format - dependent on the required function and data length - but since the fieldbus requires a fixed data length, this must be preset by means of a selection in the device description file. This length should be selected by the user such that the longest Modbus request resp. response can be processed.

8.4.3.2 Data structure



8.4.3.3 Communication sequence

The gateway always acts as the Slave with respect to the fieldbus and also acts as Slave at the Modbus end. A data exchange is always initiated by the MODBUS-Master via the RS-interface. If the Modbus-address (1st Byte) which is sent out by the Modbus-Master is identical with the address set on the gateway, the gateway sends the received data (without Modbus-address and CRC-check sum) to the fieldbus-master (look picture above). With it the gateway optionally completes as an introduction a Trigger byte and a Length byte.

The fieldbus-master detects when it has to analyse a record via the Trigger byte which is incremented by the gateway at every inquiry. The number of the following Modbus-data is to be found in the length byte.

Now the fieldbus-master has to analyse the Modbus-inquiry and it has to send back the answer in the same format (optionally with the leading Trigger byte and Length byte) via the fieldbus to the gateway.

The gateway then takes this answer and completes the Modbus-address and the CRC and sends the data to the Modbus-Master via the RS-interface. With it the data exchange is completed and the gateway waits for a new inquiry from the Modbus-Master.

8.4.4 UNIGATE[®] as Modbus-ASCII Master

On request!

-> For the description see chapter 8.4.2 "UNIGATE® as MODBUS-Master"

8.5 Protocol SSI

With the SSI protocol, e.g. SSI encoders are evaluated with the UNIGATE[®] and this information is forwarded to the higher-level controller. Parameters can be used to configure the encoder type, the encoder resolution, the clock frequency and an ERROR bit (if supported) according to the SSI encoder used. See also chapter 6 (SSI-interface).

- Resolution: The range extends from 1 bit to 25 bit. This allows single-turn SSI encoders and multi-turn SSI encoders to be configured.
- SSI Encoder Type: This can be selected between binary and gray code.
- Clock stretch: The range extends from no clock frequency to 45 kHz.
- Check Encoder: An ERROR bit can be activated here if the SSI encoder used supports it.

8.6 The trigger byte

Since the data is always transferred cyclically on PROFIBUS, the gateway must detect when the user wishes to send new data via the serial interface. This is normally done by the gateway comparing the data to be transferred via the PROFIBUS with the old data stored internally - data exchange on change (data exchange -> On Change). In many cases however, this cannot be used as the criterion, e.g. whenever the same data is to be sent. For this reason, the user can set control of transmission via a trigger byte (data exchange -> On Trigger). In this mode, the gateway always sends (and only then) when the trigger byte is changed.

Accordingly, the application program in the control in Normal mode cannot detect whether the gateway has received several identical telegrams. If Trigger-Byte mode is activated, the gateway increments the trigger byte each time a telegram has been received.

The first byte in the PROFIBUS input/output data buffer is used as the trigger byte if this mode is activated.

8.7 The length byte

The user can configure whether the transmit length is also to be stored as a byte in the input/output data area (Fieldbus lengthbyte -> active). In transmit direction, as many bytes as specified in this byte are sent. On reception of a telegram the gateway enters the number of characters received.

8.8 Protocol "Universal Modbus RTU Slave"

The UNIGATE[®] is a Modbus slave on the application side. The slave ID is set with the rotary coding switches S4 + S5 (S4 = High, S5 = Low).

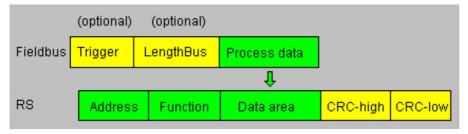
8.8.1 Data structure on the fieldbus side e.g.: PROFIBUS

Applies to In and Out

- 1. Byte: trigger byte, optional (see chapter 8.6, The trigger byte)
- 2. Byte: fieldbus length byte, optional (see chapter 8.7, The length byte)
- 3. Byte: process data
- 4. Byte: process data

....

Data structure



8.8.1.1 Example: FC1 + FC2

A Modbus Master (external device) sends a request with function code 1 or 2.

Note:

Modbus Master Request Address (High + Low) Address request 01 .. 08 will always be on address 01. Address request 09 .. 16 will always be on address 09. Address request 17 .. 24 will always be on 17. ...

Configuration:

FIELDBUS]
Fieldbus ID	126
Data exchange	On Change
Fieldbus lengthbyte	active
APPLICATION	
Protocol	Universal Modbus RTU Slave

Fieldbus sends to UNIGATE[®]

08 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 10 11 12 13 14 15 16 17 18 19 1A...

Note: The 1. byte (0x08) is the fieldbus length byte. This means only the following 8 Bytes are stored in the UNIGATE[®].

Connected Modbus Master sends request to the RS232/484 side of the UNIGATE[®]: Start-Address 0001, Length 56 (38h), FC1 (-Read Coil Status) [01] [01] [00] [00] [00] [38] [3d] [d8]

UNIGATE[®] sends response via RS232/485: [01] [01] [07] [01] [02] [03] [04] [05] [06] [07] [6b] [c5]

Display of the data in the Modbus Master (FC1):

00001: <1>	00009: <0>	00017: <1>	00025: <0>	00033: <1>	00041: <0>	00049: <1>
00002: <0>	00010: <1>	00018: <1>	00026: <0>	00034: <0>	00042: <1>	00050: <1>
00003: <0>	00011: <0>	00019: <0>	00027: <1>	00035: <1>	00043: <1>	00051: <1>
00004: <0>	00012: <0>	00020: <0>	00028: <0>	00036: <0>	00044: <0>	00052: <0>
00005: <0>	00013: <0>	00021: <0>	00029: <0>	00037: <0>	00045: <0>	00053: <0>
00006: <0>	00014: <0>	00022: <0>	00030: <0>	00038: <0>	00046: <0>	00054: <0>
00007: <0>	00015: <0>	00023: <0>	00031: <0>	00039: <0>	00047: <0>	00055: <0>
00008: <0>	00016: <0>	00024: <0>	00032: <0>	00040: <0>	00048: <0>	00056: <0>

Example: StartAddress 0008, Length 80, FC2 (Read Input Status) [01] [02] [00] [07] [00] [50] [c9] [f7]

UNIGATE[®] sends response via RS232/485: [01] [02] [0a] [02] [03] [04] [05] [06] [07] [08] [00] [00] [00] [8f] [7a]

8.8.1.2 Example: FC3 (Read Holding Register) + FC4 (Read Input Register)

Fieldbus sends to the UNIGATE®

00 30 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 10 11 12 13 14 15 16 17 18 19 1A 20 20 20...

(The configuration is "Data exchange = On Trigger", with an additonal 1. control byte in the fieldbus data.)

"Fieldbus length byte = active", in this example 30h (48d), the UNIGATE[®] copies the following 48 Byte from the fieldbus into the internal storage.

Connected Modbus Master sends request to the RS232/484 side of the UNIGATE[®] [01] [03] [00] [00] [00] [14] [45] [c5]

UNIGATE[®] sends response via RS232/485:

[01] [03] [28] [02] [03] [04] [05] [06] [07] [08] [09] [0a] [0b] [0c] [0d] [0e] [0f] [10] [11] [12] [13] [14]... ... [15] [16] [17] [18] [19] [1a]

Display of the process data in the Modbus Master:

	•
40001:	<0203H>
40002:	<0405H>
40003:	<0607H>
40004:	<0809H>
40005:	< OAOBH>
40006:	<ocodh></ocodh>
40007:	< OEOFH>
40008:	<1011H>
40009:	<1213H>
40010:	<1415H>
40011:	<1617H>
400112:	<1819H>
40013:	<1A20H>
40014:	<2020H>
40015:	<2020H>
40016:	<0000H>
40017:	<0000H>
40018:	<0000H>
40019:	<0000H>
40020:	<0000H>

Functionality FC3 and FC4 in Protocol "Universal Modbus (RTU/ASCII) Slave:

From "Universalscript Deutschmann" V1.5.1:

- FC3 (0x03): Read Holding Registers accesses Puffer Data to SPS.
- FC4 (0x04): Read Input Registers accesses Puffer Data From SPS.

8.8.1.3 Example: Write Single Coil FC5

The Fieldbus Master sent the following data to the UNIGATE[®] once: 07 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 10 11 12 13 14 15 16 17 18 19 1A 20 20 20...

1. Byte = Fieldbus length byte

The following 7 byte are stored in the UNIGATE[®], the rest is not overwritten.

With FC1 and the coil length = 80 (10 Bytes) a Modbus Master reads out the following data:

00001: <1>	00017: <1	> 00033:	<1> 0004	9: <1>	00065:	< 0 >
00002: <0>	00018: <1	> 00034: -	<0> 0005	0: <1>	00066:	< 0 >
00003: <0>	00019: <0	> 00035:	<1> 0005	1: <1>	00067:	< 0 >
00004: <0>	00020: <0	> 00036: -	<0> 0005	2: <0>	00068:	< 0 >
00005: <0>	00021: <0	> 00037: -	<0> 0005	3: <0>	00069:	< 0 >
00006: <0>	00022: <0	> 00038: -	<0> 0005	4: <0>	00070:	< 0 >
00007: <0>	00023: <0	> 00039: -	<0> 0005	5: <0>	00071:	< 0 >
00008: <0>	00024: <0	> 00040:	<0> 0005	6: <0>	00072:	< 0 >
00009: <0>	00025: <0	> 00041:	<0> 0005	7: <0>	00073:	< 0 >
00010: <1>	00026: <0	> 00042:	<1> 0005	8: <0>	00074:	< 0 >
00011: <0>	00027: <1	> 00043: -	<1> 0005	9: <0>	00075:	< 0 >
00012: <0>	00028: <0	> 00044:	<0> 0006	0: <0>	00076:	< 0 >
00013: <0>	00029: <0	> 00045: -	<0> 0006	1: <0>	00077:	< 0 >
00014: <0>	00030: <0	> 00046: -	<0> 0006	2: <0>	00078:	< 0 >
00015: <0>	00031: <0	> 00047: -	<0> 0006	3: <0>	00079:	< 0 >
00016: <0>	00032: <0	> 00048:	<0> 0006	4: < 0 > -	00080:	< 0 >

The fieldbus output data is only updated if it's triggered via a write command from the RS side. For example via FC 5 :

Write Coil	×
Node: 1	
Address: 2	
Value © Off © On	
Update Cancel	

The 1. byte is the fieldbus length byte. It contains the number of usable characters, followed by the payload. The user data (internal buffer) is no bigger than1024 byte.

In the following example the Bit (Coil) in Address 0002 is set to High (1):

Write Coil	×
Node: 1	
Address: 2	
_ Value	
O Off 💿 On	
<u>U</u> pdate Cancel	

The fieldbus data is updated: 1F 03 02 03 04 05 06 07 00 00 00 00 00

The internal buffer reserves this value, which means it can be read back by the Master via FC1 Read Coil status:

00001:	<1>
00002:	<1>
00003:	< 0 >
00004:	< 0 >
00005:	< 0 >
00006:	< 0 >
00007:	< 0 >

8.8.1.4 Example: Write Single Register FC6

Modbus Master sends the value 1234H in Address 0008:

Writ	e Register	×
	Node: 1	
	Address: 8	
	Value, (HEX): 1234	
	Update Cancel	

Der Modbus Master sends the request to the UNIGATE[®]: [01] [06] [00] [07] [12] [34] [35] [7c]

The UNIGATE[®] sends a response: [01] [06] [00] [07] [12] [34] [35] [7c]

The 2nd row shows the fieldbus data AFTER the write command.

You can see that the value 00 07 is send as Address in the Modbus request. (As mentioned in the chapter Universal Modbus Master some Master pull System one as offset.) This leads to the Byte-Offset for the fieldbus output data => 14. You start counting with the first process data value with Index NULL. 1F 03 02

+---- 1. process value

+----- fieldbus length byte

8.8.1.5 Example: Force multiple coils FC 15

Note: The address can only be passed in multiples of 8 incl. Null. Also 0, 8, 16, ... (Here you also have to keep in mind the offset of 1)

Example: Start address = 0001. Adr 0002 ... 004 was changed from Low to High

15: FORCE MULTIPLE COILS							
	Address: 0001						
		Length: 0080					
	0001:	⊙ Off ⊂ On 📥					
	0002:	C Off ⊙ On					
	0003:	○ Off ⊙ On					
	0004:	C Off 🖲 Or					
	0005:	💿 Off 🔿 On					
	0006:	🖲 Off 🔿 On					
	0007:	🖲 Off 🔿 On					
	0008:	● Off ○ On					
	<u>U</u> pda	ate <u>C</u> ancel					

Therefor the 1. process data value changed from 00h to 0Eh.

8.8.1.6 Example: Preset multiple register FC16

16: PRESET MULTIPLE REGISTERS						
Address: 0001 Length: 0080						
	0001:	OEFF	(HEX)	H	From File	
	0002:	0304	(HEX)		To File	
	0003:	0506	(HEX)			
	0004:	07FF	(HEX)			
	0005:	1205	(HEX)			
	0006:	1206	(HEX)			
	0007:	0000	(HEX)			
	0008:	0000	(HEX)			
· ·						
Update <u>C</u> ancel						

Only the content of the register address 0005 and 0006 was changed.

The 1st row shows the fieldbus BEFORE the request:

8.9 Protocol "Universal Modbus RTU Master"

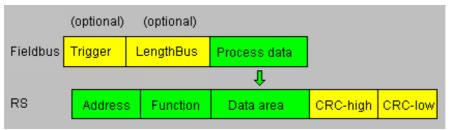
The UNIGATE[®] is Modbus-Master on the Application side.

8.9.1 Data structure Fieldbus side (e.g. PROFIBUS):

Applies to In and Out

- 1. Byte: Trigger-Byte, optional (see chapter 8.6, The trigger byte)
- 2. Byte: Fieldbus length byte, optional (see chapter 8.7, The length byte)
- 3. Process data

Data structure



8.9.2 Data structure Application side:

According to Modbus RTU Master definition.

Supported functions:

Read coil status FC1	(No. of Points = Bit)
Read input status FC2	(No. of Points = Bit)
Read multiple register FC3	(No. of Points = Word)
Read input registers FC4	(No. of Points = Word)
Force single coil FC5	(No. of Points – not used = fix 1 Bit)
Preset single register FC6	(No. of Points – not used = fix 1 Word)
Force multiple coils FC15	(No. of Points = Bit)
Preset multiple register FC16	(No. of Points = Word)

Note:

status and coil = 1 Bit, register = 16 Bit.

FC 1 + 2 as well as FC 3 + 4 are principally the same, the only difference is the definition of the start address.

At FC1 it starts at Null, at FC2 at 10 000. At FC3 it starts at 40 000, at FC4 at 30 000

8.9.3 Configuration: via Wingate since wcf Datei Version 396

Parameter Name	value range	Explanation
Modbus Timeout (10ms)	1 255 (10ms 2550ms)	Max. Waiting time for the "Response" before an error 9 is generated by timeout. If "RX Poll Retry" > 0 an error is only generated after retries.
RX Poll Retry		Retry of the last, invalid replied "Request"
RX Poll Delay (10ms)		Pause before the next "Request"

Configurations parameter for a Modbus Request:

Req. 1 Slave ID: Slave ID of the Modbus slave participant

Req. 1 Modbus Function: see "supported functions"

Req. 1 StartAdr (hex): Start address (High / Low) of the Modbus register from which should be read/written

Req. 1 No. of Points (dec): Number of the to read/to write register/coils

Req. 1 Fieldbus Map Adr(Byte): Position of the to be copied process value from/to the fieldbus range, depending on the write/read-command. If the value is NULL the process data is automatically lined up behind the other.

Up to 24 requests can be configured.

Additional configuration possibilities in the setting "Req. ... Modbus Function":

jump to Req. 1: jump to 1. request entry

disable this Req.: skip this request and perform the next request entry.

[&]quot;(10ms)" : adjustable in 10ms steps

[&]quot;(hex)": Enter in hexadecimal style.

[&]quot;(dec)": Enter in decimal style.

[&]quot;(Byte)": Counting in bytes, starting at the position Null. Attention: For read commands, e.g. FC3, after the trigger- and lenghtbyte the first process value is the position nulll, which is copied to the fieldbus to the PLC. For write commands, e.g. FC16, the position Null is the trigger byte.

8.9.3.1 Example: Read coil status FC1

Configuration

Reg. 3 Slave ID	1
Req. 3 Modbus Function	Read coil status FC1
Reg. 3 StartAdr (hex)	0004
Req. 3 No. of Points (dec)	2
Reg. 3 Fieldbus Map Adr(Byte)	6

Data content Modbus Slave

Address:	0001	Device Id: 1 MODBUS Point Type	
Length:	24	01: COIL STATUS	•
00001: <0> 00002: <0> 00003: <0> 00004: <0> 00005: <1> 00006: <0> 00006: <0> 00007: <0> 00008: <0>	00009: < 00010: < 00011: < 00012: < 00013: < 00014: < 00015: < 00016: <	0> 00018: <0> 0> 00019: <0> 0> 00020: <0> 0> 00021: <0> 0> 00022: <0> 0> 00022: <0> 0> 00023: <0>	

UNIGATE[®] reads Address 5 + 6 and copies it into the 6. byte of the output buffer.

- 1. Byte = Trigger byte (value = 0x66)
- 2. Byte = Fieldbus length byte (value = 0x07)
- 3. Byte = Fieldbus Map Adr 0 (value = 0x00)
- 4. Byte = Fieldbus Map Adr 1 (value = 0x00)
- 5. Byte = Fieldbus Map Adr 2 (value = 0x00)
- 6. Byte = Fieldbus Map Adr 3 (value = 0x00)
- 7. Byte = Fieldbus Map Adr 4 (value = 0x00)
- 8. Byte = Fieldbus Map Adr 5 (value = 0x00)
- 9. Byte = Fieldbus Map Adr 6 (value = 0x01) see configuration
- 10. Byte = Fieldbus Map Adr 7 (value = 0x00)
- 11. Byte ...

In the following example the value in address 6 in the Modbus Master is changed from 0 to 1.

00001: <0>
00002: <0>
00003: <0>
00004: <0>
00005: <1>
00006: <1>
00007: <0>
00008: <0>

The modification can be seen here:

9. Byte = Fieldbus Map Adr 6 (Wert = 0x01) => 0x03

A modification of address 7 in the Modbus slave has no consequences to the fieldbus output side because "No. Of Points = 2" is set in the configuration.

00001: <0> 00002: <0> 00003: <0> 00004: <0> 00005: <1> 00006: <1> 00007: <1> 00008: <0>

8.9.3.2 Example: Read input status FC2

The following example shows the content of address 10007 ... 10009 is mapped/copied into the 8. fieldbus output byte.

Req. 1 Slave ID	1
Reg. 1 Modbus Function	Read input status FC2
Reg. 1 StartAdr (hex)	0006
Req. 1 No. of Points (dec)	3
Reg. 1 Fieldbus Map Adr(Byte)	8

Address: Length:	0001 10	Device Id: 1 MODBUS Point Type 02: INPUT STATUS
10001: <0> 10002: <0> 10003: <0> 10004: <0> 10005: <0> 10006: <0>		
10007: <1> 10008: <0> 10009: <0> 10010: <0>		

76 09 00 00 00 00 00 00 00 00 01 00 00 00 00

			-	
Start bits	1		Device Id: 1	
Data bits	8			
Stop bits	1 /	Address: 0001	MODBUS Point Type	Inputdata
Parity	None /		02: INPUT STATUS	data(hex)
Baudrate	19200	Length: 10	· · · · · · · · · · · · · · · · · · ·	1F 09 00 00 00 00 00 00 00 00 00 05 00 00 00
232 Interface	232		-	
Modbus Master Request				21 09 00 00 00 00 00 00 00 00 00 00 00 00
Modbus Timeout (10ms)	25 /	10001: <0>		22 09 00 00 00 00 00 00 00 00 05 00 00 00 00
RX Poll Retry	0 / /	10002: <0>		
RX Poll Delay (10ms)	0 /	10003: <0>		
		10004: <0>		26 09 00 00 00 00 00 00 00 00 05 00 00 00 00
Reg. 1 Slave ID	1′ /	10005: <0>		27 09 00 00 00 00 00 00 00 00 00 05 00 00 00
Reg. 1 Modbus Function	Read input status FC2	10006: <0>		
Reg. 1 StartAdr (hex)	0006	10007: <1> LSB		29 09 00 00 00 00 00 00 00 00 00 00 00 00
Reg. 1 No. of Points (dec)	3	10008: <0>		
Reg. 1 Fieldbus Map Adr(Byte)	8	10009: <1>		
		10010: <0>		Monitor Data Diagnose DPV1_Async

Here the content of the address 10009 is changed from 0 -> 1

In the following example only the "No. Of Points" is switched to 10.

Which means that now 10 Bits => 2 Byte are read out. This is also the reason why the fieldbus length byte (2. fieldbus byte) at 0x0A increases by 1 Byte.

Start bits	1		Device Id: 1	-	
Data bits Stop bits	8	Address:	0001 MODBUS Point Type	Ш	Inputdata
	None		02: INPUT STATUS	1	data(hex)
Baudrate	19200	Length:	20	11	F1 0A 00 00 00 00 00 00 00 00 00 85 03 00 00 00 00 00 00 00 00 00 00 00 00
232 Interface	232				F2 0A 00 00 00 00 00 00 00 00 85 03 00 00 00 00 00 00 00 00 00 00 00 00
Modbus Master Request					F3 04 00 00 00 00 00 00 00 00 00 85 03 00 00 00 00 00 00 00 00 00 00 00 00
Modbus Timeout (10ms)	25				F4 04 00 00 00 00 00 00 00 00 00 85 03 00 00 00 00 00 00 00 00 00 00 00 00
RX Poll Retry	0	10001: <0>	10011: <0>		F5 0A 00 00 00 00 00 00 00 00 85 03 00 00 00 00 00 00 00 00 00 00 00 00
RX Poll Delay (10ms)	0	10002: <0>	10012: <0>		F 6 04 00 00 00 00 00 00 00 00 85 03 00 00 00 00 00 00 00 00 00 00 00 00
		10003: <0>	10013: <0> 10014: <1>		
Reg. 1 Slave ID	1	10004: <0> 10005: <0>	10014: <12		
	Read input status FC2	10005: <0>	10015: <1>		FA 04 00 00 00 00 00 00 00 00 85 03 00 00 00 00 00 00 00 00 00 00 00 00
Reg. 1 StartAdr (hex)	0006	10007: <1>	10017: <1>		FB 0A 00 00 00 00 00 00 00 00 00 00 00 00
Reg. 1 No. of Points (dec)	10	10008: <0>	10018: <0>	П	
Reg. 1 Fieldbus Map Adr(Byte)	8	10009: <1>	10019: <0>		
		10010: <0>	10020: <0>		Monitor Data Diagnose DPV1_Async

8.9.3.3 Example: Read multiple register FC3

Protocol	Universal Modbus RTU Master
Modbus Master Request	
Modbus Timeout (10ms)	25
RX Poll Retry	0
RX Poll Delay (10ms)	0
Reg. 1 Slave ID	1
Reg. 1 Modbus Function	Read multiple register FC3
Reg. 1 StartAdr (hex)	0001
Reg. 1 No. of Points (dec)	2
Reg. 1 Fieldbus Map Adr(Byte)	0

RX Poll Delay = 0 is automatically set to 1 by the firmware.

Modbus-Request:

Byte 1	Byte 2	Byte 3	Byte 4	Byte 5	Byte 6	Byte 7	Byte 8
Slave ID	Modbus Function	StartAdr High	StartAdr Low	No. of Points High	No. of Points Low	CRC High	CRC Low
1	3	0x00	0x01	0	2	х	у

The CRC value is automatically calculated by the UNIGATE®

The UNIGATE[®] sends out the request (RX Poll Retry = 0) one time via the RS interface, and waits a maximum of 250 ms (Modbus Timeout = 25) on the response.

Fieldbus Map Adr = 0 -> not activ

Thereby the addressed slave holds the following data in its registers.:

register					
address	value(hex)				
40000	0x0000				
40001	0x0202				
40002	0x0303				
40003	0x0000				
40004	0x0000				

register = 1 Word = 2 Byte



In the documentation of some applications, an Offset + 1 at the address is assumed. The notation for address "40000" stands for "holding register". But in acutality address 0x0000 is meant by it. This is not uniform in the Modbus-Slave documentations. (E.g. the PC simulation tool "ModSim32" has this offset).

If a valid response is received, the four byte (No. Of Points = 2) process value (Modbus-Data) will be copied to the fieldbus from "Fieldbus Map Adr(Byte)" = 0 on.

Fieldbus data from UNIGATE[®] -> SPS: 51 13 02 02 03 03 30 04 01 00 01 00 00 02 57 00 01 03 00 00 00 00 00 00 ...

Byte 0 = Trigger-Byte "0x51"

Byte 1 = Fieldbus length byte "0x13"

Byte 2 = Process value (High) from StartAdr "0x02"

Byte 3 = Process value (Low) from StartAdr "0x02"

Byte 4 = Process value (High) from StartAdr + 1 "0x03"

Byte 5 = Prozess value (Low) from StartAdr + 1 "0x03"

8.9.3.4 Example: Read input registers FC4

(see chapter 8.9.3.3, Example: Read multiple register FC3)

8.9.3.5 Example: Force single coil FC5

At FC5 a bit is set in the Modbus slave, if the mapped fieldbus byte is bigger (>) than NULL.

Configuration

Modbus Slave(impact)SPS sends Fieldbus data (reason)

Reg. 1 Slave ID Reg. 1 Modbus Function	1	00004: <0> 00005: <0> 00006: <1>	
Reg. 1 StartAdr (hex)	0005	00007: <0>	03 00 00 00 00 00 00 FF 00 00 00 00 00 00
Reg. 1 Fieldbus Map Adr(Byte)		00008: <0> 00009: <0>	7

Note: No. of Points is not required

Another example for when a second request is configured:



8.9.3.6 Example: Preset single register FC6

Configuration

Reg. 1 Slave ID	1
Reg. 1 Modbus Function	Preset single register FC6
Reg. 1 StartAdr (hex)	0005
Reg. 1 Fieldbus Map Adr(Byte)	7

SPS sends to UNIGATE®

UNIGATE[®] sends Modbus RTU request [01] [06] [00] [05] [ff] [23] [99] [e2]

Modbus Slave sends response [01] [06] [00] [05] [ff] [23] [99] [e2]

Storage content of Modbus Slave after Response: 40001: <0000H> 40002: <0000H> 40003: <0000H> 40004: <0000H> 40005: <0000H> 40006: <FF23 H> 40006: <0000H> 40008: <0000H> 40008: <0000H> 400010: <0000H>

8.9.3.7 Example: Force multiple coils FC15

Configuration

Reg. 1 Slave ID	1
Reg. 1 Modbus Function	Force multiple coils FC15
Reg. 1 StartAdr (hex)	0002
Reg. 1 No. of Points (dec)	10
Reg. 1 Fieldbus Map Adr(Byte)	2

Fieldbus Master sends:

UNIGATE[®] sends request: [01] [0f] [00] [02] [00] [0a] [02] [ff] [05] [65] [29]

Modbus Slave sends response: [01] [0f] [00] [02] [00] [0a] [74] [0c] Storage content of Modbus Slave after response:

00001: <0>	00011: <1>
00002: <0>	00012: <0>
00003: <1>	00013: <0>
00004: <1>	00014: <0>
00005: <1>	00015: <0>
00006: <1>	00016: <0>
00007: <1>	00017: <0>
00008: <1>	00018: <0>
00009: <1>	00019: <0>
00010: <1>	00020: <0>

Hex	FF	05
Bin		00000101
Position	87654321	11 10 9

Please keep in mind that No. Of coild = 10, hence, only the lower bit in address 0011 is written at the value 0x05. Address 0013 would already be bit No. 11, which is not transmitted anymore.

8.9.3.8 Example: Preset multiple register FC16

Configuration

Reg. 1 Slave ID	1
Reg. 1 Modbus Function	Preset multiple register FC16
Reg. 1 StartAdr (hex)	0002
Reg. 1 No. of Points (dec)	10
Reg. 1 Fieldbus Map Adr(Byte)	2

Fieldbus Master sends:

BA 00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 10 11 12 13 14 15 16 ...

UNIGATE[®] sends Request: [01] [10] [00] [02] [00] [0a] [14] [01] [02] [03] [04] [05] [06] [07] [08] [09] [0a] [0b] [0c] [0d] [0e] [0f]... ... [10] [11] [12] [13] [14] [3d] [e4]

Modbus Slave sends Response: [01] [10] [00] [02] [00] [0a] [e1] [ce]

Storage content Modbus Slave to Response:

Storage conte 40001: <0000H> 40002: <0000H> 40003: <0102H> 40004: <0304H> 40005: <0506H> 40006: <0708H> 40006: <0708H> 40006: <0B0CH) 40008: <0B0CH) 40008: <0B0CH) 40010: <0F10H> 40011: <1112H> 40012: <1314H> 40013: <0000H>

8.10 Protocol "Universal Modbus ASCII Master/Slave"

The fieldbus data exchange for Modbus ASCII is identical with RTU. The UNIGATE[®] automatically transmits the data in ASCII format on the serial side.

8.10.1 Appendix

8.10.1.1 Example Configuration 1:

Hardware UNIGATE[®] CL-PROFIBUS

WINGATE (WINGATE_395	.wcf)	_ [] >
File Options Extras Help		
more items visible	🔲 more įtems editable	
Parameter	Value	P
Software revision	V 7.8	
Device type	Profibus DP (Script)	
Script revision	36	
Serial Number	35531918	
Store Serial Number	No	
Script memory	16128	
Data memory	8192	
FIELDBUS		
Fieldbus ID	126	
Data exchange	On Trigger	
Fieldbus lengthbyte	inactive	
APPLICATION		
Protocol	Universal Modbus RTU Master	
Modbus Master Request		
Modbus Timeout (10ms)	25	
RX Poll Retry	0	
RX Poll Delay (10ms)		
	-	
Reg. 1 Slave ID	1	
Reg. 1 Modbus Function	Read multiple register FC3	
Reg. 1 StartAdr (hex)		
Reg. 1 No. of Points (dec)	2	
Reg. 1 Fieldbus Map Adr(Byte)	0	
Reg. 2 Slave ID	1	
Reg. 2 Modbus Function	Read input registers FC4	
Reg. 2 StartAdr (hex)		
Reg. 2 No. of Points (dec)	1	
Reg. 2 Fieldbus Map Adr(Byte)	4	
	-	
Reg. 3 Slave ID	1	
Reg. 3 Modbus Function	Bead coil status FC1	
Reg. 3 StartAdr (hex)		
Reg. 3 No. of Points (dec)	2	
Reg. 3 Fieldbus Map Adr(Byte)	6	
med. on loades wap Adi(byte)		
Reg. 4 Slave ID	-1	
Reg. 4 Modbus Function	Read input status FC2	
Reg. 4 StartAdr (hex)		
Reg. 4 No. of Points (dec)	2	
Reg. 4 No. or Points (dec) Reg. 4 Fieldbus Map Adr(Byte)	8	
neq. 4 rielubus Map Aul(byle)	-	
D E Cl ID		

WINGATE (WINGATE_395.	wcf)
Elle Options Extras Help	
more items visible	more items editable
Reg. 5 Slave ID	1 Development of the FC1C
Reg. 5 Modbus Function	Preset multiple register FC16 0005
Reg. 5 StartAdr (hex)	1
Req. 5 No. of Points (dec) Req. 5 Fieldbus Map Adr(Byte)	
ney, 5 rielubus map Aul(byle)	0
Reg. 6 Slave ID	1
Reg. 6 Modbus Function	Preset single register FC6 0006
Reg. 6 StartAdr (hex)	4
Req. 6 Fieldbus Map Adr(Byte)	4
Baa 3 Claure ID	2
Req. 7 Slave ID Reg. 7 Madhus Function	-
Reg. 7 Modbus Function Reg. 7 StartAdr (hex)	Read multiple register FC3
	1
Req. 7 No. of Points (dec) Reg. 7 Fieldbus Map Adr(Bvte)	12
Heq. 7 Fieldbus Map Adr(Byte)	12
Reg. 8 Slave ID	2
	Z Read input registers FC4
Reg. 8 Modbus Function Reg. 8 StartAdr (hex)	0066
Reg. 8 No. of Points (dec)	2
Reg. 8 Fieldbus Map Adr(Byte)	15
ney, o rielubus map Aul(byle)	13
Reg. 9 Slave ID	0
Reg. 9 Modbus Function	jump to Reg. 1
Reg. 9 StartAdr (hex)	
Reg. 9 No. of Points (dec)	0000
Reg. 9 Fieldbus Map Adr(Byte)	
neų, 5 rielubus map Aul(bylė)	
Reg. 10 Slave ID	0
Reg. 10 Modbus Function	jump to Reg. 1
Reg. 10 StartAdr (hex)	0000
Reg. 10 No. of Points (dec)	0
Reg. 10 Fieldbus Map Adr(Byte)	
neg. To rielabus map Adi(byle)	
Reg. 11 Slave ID	0
Reg. 11 Modbus Function	jump to Reg. 1
Reg. 11 StartAdr (hex)	
Reg. 11 No. of Points (dec)	0000
Reg. 11 Fieldbus Map Adr(Byte)	
rieg. IT Fieldbus map Aul(Byte)	0
Reg. 12 Slave ID	0

PROFIBUS Master sends and receives:

ile Options	odule Help													_0
Dutputdata		4567890abcd	Jefghijklm	nopgrstw									•	Send
hex	01.31	32 33 34 35 3	36 37 38	39 30 61	62 63 6	4 65 66	67 68 69	9 64 6	B 6C 6	5D 6E 6	5E 70 7	1 72 7	3 74 1	
nex	101 01 0												send Off	1000 ms •
											1.	Auto	send Off	1000 ms
time	length	data(hex)											data(ASC	CII)
5:36.46,034	32	00 31 32 33 3												o890abcdefghijklmr
5:36.47,048	32	00 31 32 33 6												o890abcdefghijklmr
15:36.48,062	32	01 31 32 33 6												6b890abcdefghijklm
15:36.49,076	32	01 31 32 33 6												6b890abcdefghijklm
5:36.50,090	32	01 31 32 33 3												6b890abcdefghijklm
15:36.51,104	32	00 31 32 33 3												o890abcdefghijklmr
15:36.52,117	32	31 32 33 34 3												890abcdefghijklmn
15:36.53,132	32	00 31 32 33 3												7890abcdefghijklmr
15:36.54,146	32	00 31 32 33 3												7890abcdefghijklmr
5:36.55,161	32	00 31 32 33 3											1234567	7890abcdefghijklmr
15:41.10,421	32	01 31 32 33 3	34 35 36 37	38 39 30 6	1 62 63	64 65 66	67 68 69 6	SA 68 6	C 6D 6	E 6F 70	71 72 7	374	123456	67890abcdefghijklm
d in the second s														► Í
Inputdata														
time	length		ta(hex)											
15:41.22,472	32	50	13 02 02 0											data(ASCII - Pالعام در P
15:41.22,472 15:41.23,236	32 32	50 51	13 02 02 0 13 02 02 0	3 03 30 04	01 00 01	00 00 00	02 57 00	01 03 0	0 00 0	0 00 00	00 00 0	0 00 00	00 00 00	Piin LLOJ Qiin LLOJ
15:41.22,472 15:41.23,236 15:41.23,997	32 32 32	50 51 52	13 02 02 0 13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01	00 00 00	02 57 00 02 57 00	01 03 0 01 03 0)0 00 0)0 00 0	0 00 00 0 00 00	00 00 0 00 00 0	10 00 00 10 00 00	00 00 00 00 00 00	PII11 LLOJ QII11 LLOJ BII11 LLOJ
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762	32 32 32 32 32	50 51 52 53	13 02 02 0 13 02 02 0 13 02 02 0 13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04 3 03 30 04 3 03 30 04	01 00 01 01 00 01 01 00 01	00 00 00 00 00 00 00 00 00 00 00 00 00	02 57 00 02 57 00 02 57 00	01 03 0 01 03 0 01 03 0	0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00	00 00 0 00 00 0 00 00 0	0 00 00 0 00 00 0 00 00	00 00 00 00 00 00 00 00 00	թյուլու Օլուլու Այսուլո Այսուլո
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762 15:41.25,528	32 32 32 32 32 32 32	50 51 52 53 54	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04 3 03 30 04 3 03 30 04 3 03 30 04	01 00 01 01 00 01 01 00 01 01 00 01 01 00 01		02 57 00 02 57 00 02 57 00 02 57 00	01 03 0 01 03 0 01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00	00 00 0 00 00 0 00 00 0 00 00 0	0 00 00 0 00 00 0 00 00 0 00 00	00 00 00 00 00 00 00 00 00 00 00 00	P"11 LLQU Q![11 LLQU R![11 LLQU S![11 LLQU T![11 LLQU T![11 LLQU
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762 15:41.25,528 15:41.26,294	32 32 32 32 32 32 32 32	50 51 52 53 54 55	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01 01 00 01 01 00 01 01 00 01 01 00 01		02 57 00 02 57 00 02 57 00 02 57 00 02 57 00	01 03 0 01 03 0 01 03 0 01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00 0 00 00		0 00 00 0 00 00 0 00 00 0 00 00 0 00 00	00 00 00 00 00 00 00 00 00 00 00 00 00 00	PII11 LOU QII11 LOU RII11 LOU SII11 LOU TII11 LOU UII11 LOU UII11 LOU
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762 15:41.25,528 15:41.26,294 15:41.27,053	32 32 32 32 32 32 32 32 32 32	50 51 52 53 54 55 56	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01 01 00 01 01 00 01 01 00 01 01 00 01 01 00 01		02 57 00 02 57 00 02 57 00 02 57 00 02 57 00 02 57 00 02 57 00	01 03 0 01 03 0 01 03 0 01 03 0 01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 0	00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 00		00 00 00 00 00 00 00 00 00 00 00 00 00 00	PII11 LLOU QII11 LLOU RII11 LLOU SII11 LLOU TII11 LLOU UII11 LLOU VII11 LLOU
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762 15:41.25,528 15:41.25,528 15:41.26,294 15:41.27,053 15:41.27,927	32 32 32 32 32 32 32 32 32 32 32	50 51 52 53 54 55 56 56 57	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01 01 00 01 01 00 01 01 00 01 01 00 01 01 00 01		02 57 00 02 57 00	01 03 0 01 03 0 01 03 0 01 03 0 01 03 0 01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 0			00 00 00 00 00 00 00 00 00 00 00 00 00 00	PII11 LLOU QII11 LLOU RII11 LLOU SII11 LLOU TII11 LLOU UII11 LLOU VII11 LLOU WI111 LLOU WI111 LLOU
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762 15:41.25,528 15:41.25,528 15:41.27,053 15:41.27,927 15:41.28,695	32 32 32 32 32 32 32 32 32 32 32 32 32 3	50 51 52 53 54 55 56 57 58	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01		02 57 00 02 57 00	01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 0			00 00 00 00 00 00 00 00 00 00 00 00 00 00	PII11 LLO QII11 LLO RII11 LLO SII11 LLO TI111 LLO UII11 LLO VI111 LLO WI111 LLO XI111 LLO XI111 LLO
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762 15:41.25,528 15:41.26,294 15:41.27,053 15:41.27,927 15:41.28,695 15:41.29,456	32 32 32 32 32 32 32 32 32 32 32 32 32 3	50 51 52 53 54 55 56 57 58 59 59	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01		02 57 00 02 57 00	01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 0			00 00 00 00 00 00 00 00 00 00 00 00 00 00	Ρίης 140 Qίης 140 Rίης 140 Γίης 140 Γίης 140 Uίης 140 Vίης 140 Vίης 140 Vίης 140 Vίης 140 Vίης 140 Χίης 140 Χίης 140 Υίης 140
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762 15:41.25,528 15:41.25,528 15:41.27,053 15:41.27,927 15:41.28,695	32 32 32 32 32 32 32 32 32 32 32 32 32 3	50 51 52 53 54 55 56 57 58 59 59	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01		02 57 00 02 57 00	01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 0			00 00 00 00 00 00 00 00 00 00 00 00 00 00	PII11 LLO QII11 LLO RII11 LLO SII11 LLO TI111 LLO UII11 LLO VI111 LLO WI111 LLO XI111 LLO XI111 LLO
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762 15:41.25,528 15:41.25,294 15:41.26,294 15:41.27,053 15:41.27,027 15:41.28,695 15:41.29,456 15:41.30,222	32 32 32 32 32 32 32 32 32 32 32 32 32 3	50 51 52 53 54 55 56 57 58 59 59	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01		02 57 00 02 57 00	01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 0			00 00 00 00 00 00 00 00 00 00 00 00 00 00	PI ₁₁₁ LO QI ₁₁₁ LO RI ₁₁₁ LO SI ₁₁₁ LO UI ₁₁₁ LO UI ₁₁₁ LO VI ₁₁₁ LO VI ₁₁₁ LO WI ₁₁₁ LO XI ₁₁₁ LO YI ₁₁₁ LO
15:41.22,472 15:41.23,236 15:41.23,997 15:41.24,762 15:41.25,528 15:41.26,294 15:41.27,053 15:41.27,053 15:41.27,927 15:41.28,695 15:41.29,456	32 32 32 32 32 32 32 32 32 32 32 32 32 3	50 51 52 53 54 55 56 57 58 59 54	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01		02 57 00 02 57 00	01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 0			00 00 00 00 00 00 00 00 00 00 00 00 00 00	PI ₁₇₁ LO QI ₁₇₁ LO RI ₁₇₁ LO SI ₁₇₁ LO UI ₁₇₁ LO UI ₁₇₁ LO VI ₁₇₁ LO VI ₁₇₁ LO XI ₁₇₁ LO XI ₁₇₁ LO ZI ₁₇₁ LO
15:41.22,472 15:41.23,236 15:41.23,977 15:41.24,762 15:41.25,528 15:41.25,528 15:41.27,927 15:41.27,927 15:41.28,695 15:41.29,456 15:41.30,222	32 32 32 32 32 32 32 32 32 32 32 32 32 3	50 51 52 53 54 55 56 57 58 59 54 Nose DPV1_/	13 02 02 0 13 02 02 0	3 03 30 04 3 03 30 04	01 00 01 01 00 01		02 57 00 02 57 00	01 03 0 01 03 0	0 00 0 0 00 0 0 00 0 0 00 0 0 00 0 0 00 0	0 00 00 0 00 00 0 00 00 0 00 00 0 00 00 0 00 0			00 00 00 00 00 00 00 00 00 00 00 00 00 00	PInn LLO QIInn LLO RInn LLO SInn LLO UInn LLO VInn LLO WInn LLO VInn LLO VInn LLO VInn LLO ZInn LLO

Modbus Slave storage content:

ModSim32 - ModSim1		
Elle Connection Display Window Help		
UniversalModbus01	UniversalModbus04	
Device Id: 1	Devi	ce ld: 1
Address: 0001 MODBUS Point Type	Address: 0001 MOE	BUS Point Type
03: HOLDING REGISTER	02: INPU	T STATUS 👻
Length: 20	Length: 20	
[1	
40001: <0000H> 40008: <0000H> 40015: <0000H> 40002: <0202H> 40009: <0000H> 40016: <0000H>		15: <0> 16: <0>
40003: <0303H> 40010: <0000H> 40017: <0000H>	10003: <0> 10010: <0> 100	17: <0>
40004: <0000H> 40011: <0000H> 40018: <0000H>		8: <0>
40005: <0000H> 40012: <0000H> 40019: <0000H> 40006: <0131H> 40013: <0000H> 40020: <0000H>		19: <0> 20: <0>
40007: <3435H> 40014: <0000H>	10007: <0> 10014: <0>	
		•
ModSim2	UniversalModbus03	
Address: 0256 MODBUS Point Type		BUS Point Type
Length: 5	Length: 20	STATUS 💌
	Lengui. 20	
40256: <0256H>		15: <0>
40257: <0257H>		6: <0>
40258: <0000H> 40259: <0103H>		17: <0> 18: <0>
40260: <0104H>	00005: <1> 00012: <0> 000	9: <0>
	00006: <0> 00013: <0> 0003 00007: <0> 00014: <0>	20: <0>
		E
ModSim1	UniversalModbus02	
Device Id: 2	Devi	ce ld: 1
Address: 0100 MODBUS Point Type	Address: 0001 MOE	BUS Point Type
04: INPUT REGISTER	04: INPU	T REGISTER 🔻
Length: 10	Length: 5	_
30100: <0100H> 30107: <0000H>	30001: <3001H>	
30101: <0101H> 30108: <0000H>	30002: <3002H>	
30102: <0102H> 30109: <0000H>	30003: <3003H>	
30103: <0103H> 30104: <0000H>	30004: <3004H> 30005: <0000H>	
30105: <0000H>		
30106: <0000H>		
I		

8.10.1.2 Swap Word

Configuration with "Swap Word" = enabled. Fieldbus data is swapped to the Modbus slave. Meaning High Byte and Low Byte are switched.

WINGATE (WINGATE_395	.wcf)	
File Options Extras Help		
more items visible	more items editable	
Parameter	Value	*
Software revision	V 7.8	
Device type	Profibus DP (Script)	
Script revision	36	
Serial Number	35531918	
Store Serial Number	No	
Script memory	16128	
Data memory	8192	
FIELDBUS		
Fieldbus ID	126	
Data exchange	On Trigger	
Fieldbus lengthbyte	inactive	
Ident Number (0x2079)	disabled	
Ext. Diag Off	disabled	
Swap word	enabled	
APPLICATION		
Protocol	Universal Modbus RTU Master	
Start bits	1	
Data bits	8	
Stop bits	1	
Parity	Even	
Baudrate	19200	
232 Interface	232	
Modbus Master Request		
Modbus Timeout (10ms)	25	
RX Poll Retry	0	
RX Poll Delay (10ms)	0	
Reg. 1 Slave ID	1	
Reg. 1 Modbus Function	Read multiple register FC3	
Reg. 1 StartAdr (hex)	0001	
Reg. 1 No. of Points (dec)	5	
Reg. 1 Fieldbus Map Adr(Byte)	0	
Req. 2 Slave ID	1	
Reg. 2 Modbus Function	Preset multiple register FC16	
Reg. 2 StartAdr (hex)	0001	
Reg. 2 No. of Points (dec)	5	
Reg. 2 Fieldbus Map Adr(Byte)	0	
	-	
Req. 3 Slave ID	1	
Reg. 3 Modbus Function	jump to Req. 1	
Reg. 3 StartAdr (hex)	0004	-
78670379005000240100021	E2C8E7E00000000000000000000000000000000000	

Fieldbus Master sends and receives.

The swapping is bidirectional

Profibus Me	odule				_0
ile <u>O</u> ptions	Help				
outputdata	\h00\\h0\\h	03\\h1\1234567890abcdefghijklmnopqr		-	<u>S</u> end
hex	00 00 03 01	31 32 33 34 35 36 37 38 39 30 61 62 63	8 64 65 66 67 68 69 6A 6B 6C 6D	6E 6F 70 71 7	
				Autosend Off	1000 ms
time	length	data(hex) d	lata(ASCII)		
6:42.43.547	32	00 00 FF 00 31 32 33 34 35 36 37 38	ii 1234567890abcdefahiiklmnopar		
6:43.23,086	32	FF 00 FF 00 31 32 33 34 35 36 37 38 ÿ	ü 1234567890abcdefghijklmnopgr		
6:46.34.463	32		ÿ 1234567890abcdefqhijklmnopgr		
6:47.00,451	32		ü 1234567890abcdefghiiklmnopgr		
6:54.51,690	32		i 1234567890abcdefghijklmnopgr		
6:55.29.201	32	01 00 FF FF 31 32 33 34 35 36 37 38	iii1234567890abcdefahiiklmnopar		
6:55.49,369	32	00 00 0F FF 31 32 33 34 35 36 37 38	¢ÿ1234567890abcdefghijklmnopgr		
6:56.18.977	32	02 00 01 FF 31 32 33 34 35 36 37 38	ÿ1234567890abcdefahiiklmnopar		
6:56.48.360	32	03 00 01 01 31 32 33 34 35 36 37 38 4			
	32	04 00 03 01 31 32 33 34 35 36 37 38 4			
17:02:15:684	32	00 00 03 01 31 32 33 34 35 36 37 38			
	32	00 00 03 01 31 32 33 34 35 36 37 38	L 1234567890abcdefghijklmnopqr		
17:02.15,684	32	00 00 03 01 31 32 33 34 35 36 37 38	 123456789Uabcdetghijklimnopgr 		
	32	00 00 03 01 31 32 33 34 35 36 37 38	 123456789Uabcdefghijklimnopgr 		
(32	00 00 03 01 31 32 33 34 35 36 37 38	 1234567850abcdetghijklimnopqr 		
			 123436789Uabcdetghijklimnopqr 		
nputdata time	length	data(hex)		data(ASCII)	
nputdata time 17:03.43,242	length 32	dəta(hex) 23 0A 00 00 03 01 31 32 33 34 35 36 00 0	0 00 00 00 00 00 00 00 00 00 00 00 00 0	# - 123456	
nputdata time 17:03.43,242 17:03.43,459	length 32 32	data(hex) 23 (04 00 00 03 01 31 32 33 34 35 36 00 0 24 (04 00 00 03 01 31 32 33 34 35 36 00 0		# - 123456 \$ - 123456	
nputdata time 17:03.43,242 17:03.43,459	length 32 32 32	dəta(hex) 23 0A 00 00 03 01 31 32 33 34 35 36 00 0		# - 123456	
nputdata time 17:03.43,242 17:03.43,459 17:03.43,678	length 32 32	data(hex) 23 (04 00 00 03 01 31 32 33 34 35 36 00 0 24 (04 00 00 03 01 31 32 33 34 35 36 00 0		# - 123456 \$ - 123456	
nputdata time 17:03.43,242 17:03.43,459 17:03.43,678 17:03.43,897	length 32 32 32	data[hex] 23 0A 00 00 30 1 31 32 33 34 35 36 00 0 24 0A 00 00 03 01 31 32 33 34 35 36 00 0 25 0A 00 00 03 01 31 32 33 34 35 36 00 0		# L 123456 \$ L 123456 % L 123456	
nputdata time 17:03.43,242 17:03.43,459 17:03.43,897 17:03.43,897 17:03.44,118	length 32 32 32 32 32	data(hex) 23 0A 00 00 03 01 31 32 33 34 35 36 00 0 24 0A 00 00 03 01 31 32 33 34 35 36 00 0 25 0A 00 00 03 01 31 32 33 34 35 36 00 0 26 0A 00 00 03 01 31 32 33 34 35 36 00 0		# L 123456 \$ L 123456 % L 123456 & L 123456 & L 123456	
inputdata time 17:03.43,242 17:03.43,459 17:03.43,678 17:03.43,678 17:03.44,118 17:03.44,134	length 32 32 32 32 32 32 32	data(hex) 23 (04 00 00 03 01 31 32 33 34 35 36 00 0 24 (04 00 00 03 01 31 32 33 34 35 36 00 0 25 (04 00 00 03 01 31 32 33 34 35 36 00 0 26 (04 00 00 03 01 31 32 33 34 35 36 00 0 27 (04 00 00 03 01 31 32 33 34 35 36 00 0 27 (04 00 00 03 01 31 32 33 34 35 36 00 0		# L 123456 \$ L 123456 % L 123456 & L 123456 & L 123456 ' L 123456	
inputdata time 17:03.43,242 17:03.43,459 17:03.43,678 17:03.43,878 17:03.44,184 17:03.44,334 17:03.44,443	length 32 32 32 32 32 32 32 32	data(hex) 23 (A) 00 00 03 01 31 32 33 34 35 36 00 0 24 (A) 00 00 03 01 31 32 33 34 35 36 00 0 25 (A) 00 00 03 01 31 32 33 34 35 36 00 0 26 (A) 00 00 03 01 31 32 33 34 35 36 00 0 27 (A) 00 00 03 01 31 32 33 34 35 36 00 0 28 (A) 00 00 03 01 31 32 33 34 35 36 00 0 28 (A) 00 00 03 01 31 32 33 34 35 36 00 0		# L 123456 \$ L 123456 \$ L 123456 & L 123456 L 123456 (L 123456 (L 123456	
() Inputdata	length 32 32 32 32 32 32 32 32 32 32 32	data(hex) 23 QA 00 00 03 01 31 32 33 34 35 36 00 0 24 QA 00 00 03 01 31 32 33 34 35 36 00 0 25 QA 00 00 03 01 31 22 33 34 35 36 00 0 26 QA 00 00 30 11 32 33 34 35 36 00 0 27 QA 00 00 03 01 31 32 33 34 35 36 00 0 28 QA 00 00 03 01 31 32 33 34 35 36 00 0 28 QA 00 00 03 01 31 32 33 34 35 36 00 0 28 QA 00 00 03 01 31 32 33 34 35 36 00 0		# L 123456 \$ L 123456 \$ L 123456 & L 123456 & L 123456 (L 123456 (L 123456) L 123456	
(nputdata time 17:03.43,242 17:03.43,678 17:03.43,678 17:03.43,897 17:03.44,118 17:03.44,433 17:03.44,433 17:03.44,664	length 32 32 32 32 32 32 32 32 32 32 32 32 32	data[hex] 23 0A 00 00 30 1 31 32 33 34 35 36 00 0 24 0A 00 00 30 1 31 32 33 34 35 36 00 0 25 0A 00 00 30 1 31 32 33 34 35 36 00 0 26 0A 00 00 30 1 31 32 33 34 35 36 00 0 26 0A 00 00 30 1 31 22 33 34 35 36 00 0 27 0A 00 00 30 1 31 22 33 34 35 36 00 0 28 0A 00 00 30 1 31 32 33 34 35 36 00 0 28 0A 00 00 30 1 31 32 33 34 35 36 00 0 29 0A 00 00 30 1 31 32 33 34 35 36 00 0 29 0A 00 00 03 01 31 32 33 34 35 36 00 0 29 0A 00 00 03 01 31 32 33 34 35 36 00 0 29 0A 00 00 03 01 31 32 33 34 35 36 00 0 29 0A 00 00 03 01 31 32 33 34 35 36 00 0		# L 123456 \$ L 123456 % L 123456 & L 123456 & L 123456 (L 123456 L 123456 * L 123456 * L 123456	
inputdata time 17:03.43,242 17:03.43,459 17:03.43,678 17:03.43,678 17:03.43,4118 17:03.44,118 17:03.44,431 17:03.44,678	length 32 32 32 32 32 32 32 32 32 32 32 32 32	data(hex) 23 QA 00 00 03 01 31 32 33 34 35 36 00 0 24 QA 00 00 03 01 31 32 33 34 35 36 00 0 25 QA 00 00 03 01 31 22 33 34 35 36 00 0 26 QA 00 00 03 01 31 22 33 34 35 36 00 0 26 QA 00 00 03 01 31 22 33 34 35 36 00 0 27 QA 00 00 03 01 31 22 33 34 35 36 00 0 28 QA 00 00 03 01 31 22 33 34 35 36 00 0 28 QA 00 00 03 01 31 32 33 34 35 36 00 0 29 QA 00 00 03 01 31 32 33 34 35 36 00 0 28 QA 00 00 03 01 31 32 33 34 35 36 00 0 28 QA 00 00 03 01 31 32 33 34 35 36 00 0 28 QA 00 00 03 01 31 32 33 34 35 36 00 0 28 QA 00 00 03 01 31 32 33 34 35 36 00 0		# L 123456 \$ L 123456 \$ L 123456 & L 123456 (L 123456 (L 123456) L 123456) L 123456 * L 123456 * L 123456 + L 123456 , L 123456	
(nputdata time 17:03.43,242 17:03.43,459 17:03.43,678 17:03.44,384 17:03.44,384 17:03.44,34 17:03.44,433 17:03.44,688 17:03.45,102	length 32 32 32 32 32 32 32 32 32 32 32 32 32	data(hex) 23 QA 00 00 30 11 31 22 33 34 35 36 00 0 24 QA 00 00 30 11 32 33 34 35 36 00 0 25 QA 00 00 30 11 31 22 33 34 35 36 00 0 26 QA 00 00 30 11 31 22 33 34 35 36 00 0 27 QA 00 00 30 11 31 22 33 34 35 36 00 0 28 QA 00 00 30 11 31 22 33 34 35 36 00 0 29 QA 00 00 30 11 31 22 33 34 35 36 00 0 29 QA 00 00 30 11 31 22 33 34 35 36 00 0 29 QA 00 00 30 11 31 22 33 34 35 36 00 0 29 QA 00 00 30 11 31 22 33 34 35 36 00 0 20 QA 00 00 30 11 31 22 33 34 35 36 00 0 20 QA 00 00 03 01 31 32 23 34 35 36 00 0 20 QA 00 00 03 01 31 32 23 34 35 36 00 0 20 QA 00 00 03 01 31 32 23 34 35 36 00 0 20 QA 00 00 03 01 31 32 23 34 35 36 00 0 20 QA 00 00 03 01 31 32 23 34 35 36 00 0 20 QA 00 00 03 01 31 32 23 34 35 36 00 0		# L 123456 \$ L 123456 \$ L 123456 & L 123456 (L 123456 (L 123456) L 123456) L 123456 * L 123456 * L 123456 + L 123456 , L 123456	
nputdata time 17:03.43,242 17:03.43,459 17:03.44,3459 17:03.44,343 17:03.44,118 17:03.44,118 17:03.44,118 17:03.44,118 17:03.44,118 17:03.44,118 17:03.44,118 17:03.44,118 17:03.45,118 4 17:03.45,118 17:03.45,1	length 32 32 32 32 32 32 32 32 32 32 32 32 32	data(hex) 23 0A 00 00 03 01 31 32 33 34 35 36 00 0 24 0A 00 00 03 01 31 32 33 34 35 36 00 0 25 0A 00 00 30 1 31 22 33 34 35 36 00 0 26 0A 00 00 30 1 31 22 33 34 35 36 00 0 26 0A 00 00 30 1 31 22 33 34 35 36 00 0 27 0A 00 00 30 1 31 32 33 34 35 36 00 0 28 0A 00 00 30 1 31 32 33 34 35 36 00 0 29 0A 00 00 30 1 31 32 33 34 35 36 00 0 29 0A 00 00 30 1 31 32 33 34 35 36 00 0 28 0A 00 00 30 1 31 22 33 34 35 36 00 0 28 0A 00 00 30 1 31 32 33 34 35 36 00 0 20 A 00 00 30 1 31 32 33 34 35 36 00 0 20 A 00 00 03 01 31 32 33 34 35 36 00 0 20 A 00 00 30 1 31 32 33 34 35 36 00 0 20 A 00 00 30 1 31 32 33 34 35 36 00 0 20 A 00 00 30 1 31 32 33 34 35 36 00 0		# L 123456 \$ L 123456 \$ L 123456 & L 123456 (L 123456 (L 123456) L 123456) L 123456 * L 123456 * L 123456 + L 123456 , L 123456	
nputdata time 17:03.43,242 17:03.43,678 17:03.43,678 17:03.44,3897 17:03.44,3897 17:03.44,443 17:03.44,443 17:03.44,678 17:03.44,5102 17:03.45,102	length 32 32 32 32 32 32 32 32 32 32 32 32 32	data(hex) 23 0A 00 00 03 01 31 32 33 34 35 36 00 0 24 0A 00 00 03 01 31 32 33 34 35 36 00 0 25 0A 00 00 30 1 31 22 33 34 35 36 00 0 26 0A 00 00 03 01 31 22 33 34 35 36 00 0 26 0A 00 00 30 1 31 22 33 34 35 36 00 0 27 0A 00 00 30 1 31 32 33 34 35 36 00 0 28 0A 00 00 30 1 31 32 33 34 35 36 00 0 29 0A 00 00 30 1 31 32 33 34 35 36 00 0 29 0A 00 00 30 11 31 22 33 34 35 36 00 0 20 A 00 00 03 01 31 32 33 34 35 36 00 0 20 A 00 00 03 01 31 32 33 34 35 36 00 0 20 A 00 00 03 01 31 32 33 34 35 36 00 0 20 D A 00 00 30 1 31 32 33 34 35 36 00 0 20 D A 00 00 30 1 31 32 33 34 35 36 00 0 20 D A 00 00 30 1 31 32 33 34 35 36 00 0 20 D A 00 00 30 1 31 32 33 34 35 36 00 0 20 D A 00 00 30 1 31 32 33 34 35 36 00 0 20 D A 00 00 30 1 31 32 33 34 35 36 00 0 20 D A 00 00 30 1 31 32 33 34 35 36 00 0		# L 123456 \$ L 123456 \$ L 123456 & L 123456 (L 123456 (L 123456) L 123456) L 123456 * L 123456 * L 123456 + L 123456 , L 123456	

Modbus Slave Data content

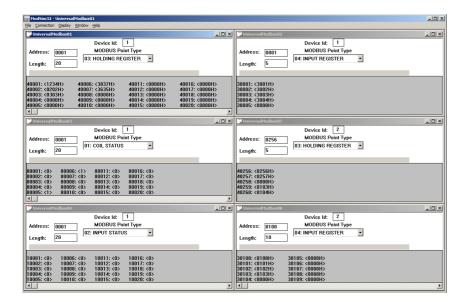
ModSim32 - UniversalModbus01	
<u>Ele Connection Display Window H</u> elp	
UniversalModbus01	UniversalModbus04
Device Id: 1 Address: 0001 MODBUS Point Type 03: HOLDING REGISTER ▼ Length: 20	Device Id: 1 Address: 0001 MODBUS Point Type 102: INPUT STATUS
40001: 40008: 40015: 400015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: 40015: </th <td>10001: (0) 10008: (0) 10015: (0) 10002: (0) 10009: (0) 10016: (0) 10003: (0) 10011: (0) 10017: (0) 10004: (0) 10011: (0) 10018: (0) 10005: (1) 10011: (0) 100115: (0) 10005: (1) 100115: (0) 100115: (0) 10005: (1) 100112: (2) 100115: (0) 10005: (1) 100112: (2) 100115: (0) 10007: (3) 10014: (2) 100120: (3)</td>	10001: (0) 10008: (0) 10015: (0) 10002: (0) 10009: (0) 10016: (0) 10003: (0) 10011: (0) 10017: (0) 10004: (0) 10011: (0) 10018: (0) 10005: (1) 10011: (0) 100115: (0) 10005: (1) 100115: (0) 100115: (0) 10005: (1) 100112: (2) 100115: (0) 10005: (1) 100112: (2) 100115: (0) 10007: (3) 10014: (2) 100120: (3)
UniversalModbus07	
Address: 0256 MODBUS Point Type Length: 5 03: HOLDING REGISTER •	Device Id: 1 Address: 0001 MODBUS Point Type Length: 20 01: COIL STATUS
40256; <0256H> 40257; <0257H> 40258; <0000H> 40259; <0103H> 40259; <0103H> 40250; <0104H>	00001: (0> 00008: (0> 00015: (0> 00002: (1> 00009: (0> 00016: (0> 00003: (1> 00019: (1> 00017: (0) 00004: (0> 00011: (0> 00018: (0> 00005: (0> 00012: (0> 00019: (0> 00006: (0> 00013: (0> 00020: (0> 000007: (0> 00014: (0>
Universalt/todbus08 Device Id: 2 Address: 0100 Length: 10 Lengt	X Device Id: 1 Address: 0001 MODBUS Point Type Length: 5
30100: <0100H> 30107: <0000H> 30101: <0101H> 30108: <0000H> 30102: <0102H> 30108: <0000H> 30103: <0103H> 30104: <0000H> 30105: <0000H> 30105: <0000H> 4	30001: <3001H> 30002: <3002H> 30003: <3003H> 30004: <3004H> 30006: <0000H>
كتدر	

8.10.1.3 Example with Fast Ethernet

📲 WINGATE (WINGATE_395	.wcf)	_ 🗆 ×
Eile Options Extras Help		
☑ more items visible	🔽 more items editable	
Parameter	Value	-
Software revision	V 5.4	
Device type	Fast Ethernet(Script)	
Script revision	39	
Serial Number	36110091	
Store Serial Number	No	
Script memory	16128	
Data memory	8192	
FIELDBUS		
IP Address UNIGATE	172.16.48.210	
Subnet-Mask	255.255.255.0	
IP Address Gateway	0.0.0.0	
IP Address Target	0.0.0	
Transport protocol	UDP	
Send Port (dec)	23	
Receive Port (dec)	23	
Blocklength fieldbus input	255	
Blocklength fieldbus output	255	
Data exchange Fieldbus lengthbyte	On Trigger active	
	disabled	
Swap word APPLICATION	disabled	
Protocol	Universal Modbus BTU Master	
Start bits	1	
Data bits	8	
Stop bits	1	
Parity	Even	
Baudrate	19200	
232 Interface	232	
- Modbus Master Request		
Modbus Timeout (10ms)	25	
RX Poll Retry	0	
RX Poll Delay (10ms)	0	
Reg. 1 Slave ID	1	
Reg. 1 Modbus Function	Read multiple register FC3	
Reg. 1 StartAdr (hex)	0001	
Reg. 1 No. of Points (dec)	2	
Req. 1 Fieldbus Map Adr(Byte)	0	

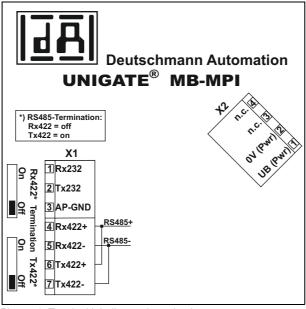
WINGATE (WINGATE_395.	wcf)
Eile Options Extras Help	
✓ more items visible	✓ more items editable
ie more icerns visible	I more items editable
Req. 2 Slave ID	1
Reg. 2 Modbus Function	Read input registers FC4
Reg. 2 StartAdr (hex)	0003
Req. 2 No. of Points (dec)	1
Reg. 2 Fieldbus Map Adr(Byte)	4
Reg. 3 Slave ID	1
Reg. 3 Modbus Function	Read coil status FC1
Reg. 3 StartAdr (hex)	0004
Req. 3 No. of Points (dec)	2
Req. 3 Fieldbus Map Adr(Byte)	6
Reg. 4 Slave ID	1
Reg. 4 Modbus Function	Read input status FC2
Reg. 4 StartAdr (hex)	0004
Reg. 4 No. of Points (dec)	2
Req. 4 Fieldbus Map Adr(Byte)	8
Req. 5 Slave ID	1
Reg. 5 Modbus Function	Preset multiple register FC16
Reg. 5 StartAdr (hex)	0005
Req. 5 No. of Points (dec)	1
Req. 5 Fieldbus Map Adr(Byte)	2
Reg. 6 Slave ID	1
Reg. 6 Modbus Function	Preset single register FC6
Reg. 6 StartAdr (hex)	0006
Req. 6 No. of Points (dec)	4
Req. 6 Fieldbus Map Adr(Byte)	4
Reg. 7 Slave ID	2
Reg. 7 Modbus Function	Read multiple register FC3
Reg. 7 StartAdr (hex)	0100
Reg. 7 No. of Points (dec)	1
Reg. 7 Fieldbus Map Adr(Byte)	12
Reg. 8 Slave ID	2
549F097D005C0027010002CE	D843AC1030DCFFFFFF0000000003F0020002199000100000E00000001001

- Etherneeth	odule							
ile Options	Help							
outputdata	0987	654321				•		<u>S</u> end
hex	30 39	38 37 36 35 34 33 32	31					
			Γ	Increment Auto	orepeat off	1000 ms	•	Repeat
time	length	data(hex)	data(ASCII)					-
0:27.33,411	15	2D 2D 2D 2D 2D 2D 2D .						
0:31.45,148	15	2D 2D 2D 2D 2D 2D 2D .						
0:34.48,449	15	2D 2D 2D 2D 2D 2D 2D .						
0:35.21,774	15	2D 2D 2D 2D 2D 2D 2D .						
0:35.33,776	16	30 2D 2D 2D 2D 2D 2D						
0:35.53,704	28	30 31 31 31 31 31 31 31.		111111111111111111				
0:36.01,936	28	31 31 31 31 31 31 31 31. 30 39 38 37 36 35 34.		1111111111111111111	1			-
0:36.22,127	10	30 39 38 37 36 35 34.	0987654321					
(
<u>[]</u>								
nputdata								
nputdata time	len	data(hex)				data(ASCII)		
iputdata time 10:37.38,616	21	C2 13 02 02 03 03 30 0				Âl _{hi} LUU L		
nputdata time 10:37.38,616 10:37.39,427	21 21	C2 13 02 02 03 03 30 0 C3 13 02 02 03 03 30 0	4 03 00 00 00 00	00 02 57 00 01 03 0	0 00	Â _{‼ηη} τι <u>Ο</u> ι Ά _{‼ηη} τιΟι τ		
nputdata time 10:37.38,616 10:37.39,427 10:37.40,236	21 21 21	C2 13 02 02 03 03 30 0 C3 13 02 02 03 03 30 0 C4 13 02 02 03 03 30 0	4 03 00 00 00 00 00 4 03 00 00 00 00 00	00 02 57 00 01 03 0 00 02 57 00 01 03 0	i0 00 i0 00	Âι ₁₁ τιμι Άι ₁₁ τιμι Άι ₁₁ τιμι		
nputdata time 10:37.38,616 10:37.39,427 10:37.40,236 10:37.41,048	21 21 21 21	C2 13 02 02 03 03 30 0 C3 13 02 02 03 03 30 0 C4 13 02 02 03 03 30 0 C5 13 02 02 03 03 30 0 C5 13 02 02 03 03 30 0	4 03 00 00 00 00 00 4 03 00 00 00 00 00 4 03 00 00 00 00 00	00 02 57 00 01 03 0 00 02 57 00 01 03 0 00 02 57 00 01 03 0	10 00 10 00 10 00	Â _{llηη} τιριτ Ά _{llηη} τιριτ Ä _{llηη} τιριτ Å _{llηη} τιριτ		
nputdata time 10:37.38,616 10:37.39,427 10:37.40,236 10:37.41,048 10:37.41,860	21 21 21 21 21 21	C2 13 02 02 03 03 30 0 C3 13 02 02 03 03 30 0 C4 13 02 02 03 03 30 0 C5 13 02 02 03 03 30 0 C5 13 02 02 03 03 30 0 C6 13 02 02 03 03 30 0	4 03 00 00 00 00 00 4 03 00 00 00 00 00 4 03 00 00 00 00 00 4 03 00 00 00 00 00	00 02 57 00 01 03 0 00 02 57 00 01 03 0	10 00 10 00 10 00 10 00	Â ₁₁₁ ιιοι ι Ά ₁₁₁ ιιοι ι Ά ₁₁₁ ιιοι ι Å ₁₁₁ ιιοι ι Æ ₁₁₁ ιιοι ι		
nputdata time 10:37.38,616 10:37.39,427 10:37.40,236 10:37.41,048 10:37.41,860 10:37.42,669	21 21 21 21 21 21 21	C2 13 02 02 03 03 30 0 C3 13 02 02 03 03 30 0 C4 13 02 02 03 03 30 0 C5 13 02 02 03 03 30 0 C5 13 02 02 03 03 30 0 C6 13 02 02 03 03 30 0 C7 13 02 02 03 03 30 0	4 03 00 00 00 00 00 4 03 00 00 00 00	00 02 57 00 01 03 0 00 02 57 00 01 03 0	10 00 10 00 10 00 10 00 10 00	Â ₁₁₁ ιιου ι Â ₁₁₁ ιιου ι À ₁₁₁ ιιου ι Â ₁₁₁ ιιου ι Æ ₁₁₁ ιιου ι Ç ₁₁₁ ιιου ι		
putdata time 10:37.39,616 10:37.39,427 10:37.40,236 10:37.41,048 10:37.41,060 10:37.42,669 10:37.43,684	21 21 21 21 21 21 21 21	C2 13 02 02 03 03 30 0 C3 13 02 02 03 03 30 0 C4 13 02 02 03 03 30 0 C5 13 02 02 03 03 30 0 C6 13 02 02 03 03 30 0 C6 13 02 02 03 03 30 0 C7 13 02 02 03 03 30 0 C8 13 02 02 03 03 33 0 0	4 03 00 00 00 00 4 05 00 00 00 00 4 FF FF 00 00 00	00 02 57 00 01 03 0 00 02 57 00 01 03 0	0 00 0 00 0 00 0 00 0 00 0 00	Âι ₁₁ τιου τ Âι ₁₁ τιου τ Âι ₁₁ τιου τ Âι ₁₁ τιου τ Æι ₁₁ τιου τ Œι ₁₁ τιου τ Ėι ₁₁ τιου τ Èι ₁₁ τιου ijj		
nputdata time 10:37.38,616 10:37.39,427 10:37.40,236 10:37.41,048 10:37.41,860 10:37.42,669 10:37.43,684 10:37.44,494	21 21 21 21 21 21 21 21 21	C2 13 02 02 03 03 30 0 C3 13 02 02 03 03 30 0 C4 13 02 02 03 03 30 0 C5 13 02 02 03 03 30 0 C6 13 02 02 03 03 30 0 C7 13 02 02 03 03 30 0 C8 13 02 02 03 03 30 0 C8 13 02 02 03 03 30 0 C9 13 02 02 03 03 30 0	4 03 00 00 00 00 4 FF FF 00 00 00 4 03 00 00 00 00	$\begin{array}{c} 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0\\ 00 \ 02 \ 01 \ 03 \ 0\\ 00 \ 02 \ 01 \ 03 \ 0\\ 00 \ 02 \ 01 \ 03 \ 0\\ 00 \ 01 \ 03 \ 0\\ 00 \ 01 \ 03 \ 0\\ 00 \ 01 \ 03 \ 0\\ 00 \ 00 \ 01 \ 03 \ 0\\ 00 \ 00 \ 01 \ 03 \ 0\\ 00 \ 00 \ 01 \ 03 \ 0\\ 0\ 00 \ 00 \ 01 \ 03 \ 0\\ 0\ 00 \ 00 \ 01 \ 03 \ 0\\ 0\ 00 \ 00 \ 01 \ 03 \ 0\\ 0\ 00 \ 01 \ 03 \ 0\\ 0\ 00 \ 00 \ 00 \ 00 \ 00 \ 0$	0 00 0 00 0 00 0 00 0 00 0 00 0 00 0 0	Âι ₁₁₁ τιου τ Âι ₁₁₁ τιου τ Âι ₁₁₁ τιου τ Âι ₁₁₁ τιου τ Æι ₁₁₁ τιου τ Ει ₁₁₁ τιου τ Ėι ₁₁₁ τιου τ		
hputdata time 10:37.38,616 10:37.39,427 10:37.40,236 10:37.41,860 10:37.41,860 10:37.42,669 10:37.43,684 10:37.43,684 10:37.45,306	21 21 21 21 21 21 21 21 21 21	$\begin{array}{c} C2 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C3 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C5 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C6 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C7 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C9 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 02 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 03 \ 03 \ 03 \ 03 \ 0$	4 03 00 00 00 00 4 FF FF 00 00 00 4 03 00 00 00 00 4 03 00 00 00 00	$\begin{array}{c} 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 01 \ 03 \ 0 \\ 00 \ 01 \ 03 \ 0 \\ 00 \ 01 \ 03 \ 0 \ 01 \ 03 \ 0 \\ 00 \ 01 \ 03 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 01 \ 03 \ 00 \ 01 \ 03 \ 0 \ 00 \ 0$	0 00 0 00 0 00 0 00 0 00 0 00 0 00 0 0	Âlını (LQ) (Âlını (LQ) (Àlını (LQ) (Àlını (LQ) (Âlını (LQ) (Çlını (LQ) (Êlını (LQ) (Êlını (LQ) (Êlını (LQ) (
nputdata time 10:37.39.427 10:37.40.236 10:37.41.048 10:37.41.048 10:37.42.669 10:37.43.684 10:37.45.306 10:37.46.119	21 21 21 21 21 21 21 21 21 21 21 21	$\begin{array}{c} C2 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C3 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 00 \\ C5 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C7 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C7 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C3 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C3 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 03 \ 03 \ 00 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 03 \ 03 \ 00 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 03 \ 03 \ 00 \ 02 \ 03 \ 03 \ 0 \ 0 \ 03 \ 00 \ 03 \ 00 \ 00 \ 03 \ 0 \ 0$	4 03 00 00 00 00 00 4 03 00 00 00 00	$\begin{array}{c} 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \ 01 \ 03 \ 0 \ 00 \ 0$	0 00 0 00 0 00 0 00 0 00 0 00 0 00 0 0	Âι,, ιομι Άι, ιομι Άι, ιομι Δι, ιομι Δι, ιομι Δι, ιομι Ει, ιομι Ει, ιομι Ει, ιομι Ει, ιομι		
nputdata time 10:37.38,616 10:37.39,427 10:37.40,236 10:37.41,048 10:37.41,860 10:37.42,669 10:37.43,684 10:37.43,684 10:37.45,306	21 21 21 21 21 21 21 21 21 21	$\begin{array}{c} C2 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C3 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C5 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C7 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C7 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C9 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 02 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \\ C4 \ 03 \ 03 \ 03 \ 0 \ 0 \ 0 \ 03 \ 03 \$	4 03 00 00 00 00 00 4 03 00 00 00 00	$\begin{array}{c} 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \ 01 \ 03 \ 0 \ 00 \ 0$	0 00 0 00 0 00 0 00 0 00 0 00 0 00 0 0	Âlını (LQ) (Âlını (LQ) (Àlını (LQ) (Àlını (LQ) (Âlını (LQ) (Çlını (LQ) (Êlını (LQ) (Êlını (LQ) (Êlını (LQ) (
nputdata time 10:37.39.427 10:37.40.236 10:37.41.048 10:37.41.048 10:37.42.669 10:37.43.684 10:37.45.306 10:37.46.119	21 21 21 21 21 21 21 21 21 21 21 21	$\begin{array}{c} C2 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C3 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 00 \\ C5 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C7 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C7 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C3 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C3 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 03 \ 03 \ 00 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 03 \ 03 \ 00 \ 02 \ 03 \ 03 \ 0 \ 0 \\ C4 \ 03 \ 03 \ 00 \ 02 \ 03 \ 03 \ 0 \ 0 \ 03 \ 00 \ 03 \ 00 \ 00 \ 03 \ 0 \ 0$	4 03 00 00 00 00 00 4 03 00 00 00 00	$\begin{array}{c} 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \ 01 \ 03 \ 0 \ 00 \ 0$	0 00 0 00 0 00 0 00 0 00 0 00 0 00 0 0	Âι,, ιομι Άι, ιομι Άι, ιομι Δι, ιομι Δι, ιομι Δι, ιομι Ει, ιομι Ει, ιομι Ει, ιομι Ει, ιομι		
hputdata time 10:37.39.427 10:37.40.236 10:37.41.048 10:37.41.048 10:37.42.669 10:37.43.684 10:37.44.394 10:37.45.306 10:37.46.119	21 21 21 21 21 21 21 21 21 21 21 21	$\begin{array}{c} C2 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C3 \ 13 \ 02 \ 02 \ 03 \ 03 \ 30 \ 0 \\ C4 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C5 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C5 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C6 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C8 \ 13 \ 02 \ 02 \ 03 \ 33 \ 0 \\ C1 \ 33 \ 02 \ 03 \ 03 \ 0 \\ \end{array}$	4 03 00 00 00 00 00 4 03 00 00 00 00	$\begin{array}{c} 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \ 01 \ 03 \ 0 \\ 00 \ 02 \ 57 \ 00 \ 01 \ 03 \ 0 \ 01 \ 03 \ 0 \ 00 \ 0$	0 00 0 00 0 00 0 00 0 00 0 00 0 00 0 0	Âι,, ιομι Άι, ιομι Άι, ιομι Δι, ιομι Δι, ιομι Δι, ιομι Ει, ιομι Ει, ιομι Ει, ιομι Ει, ιομι		

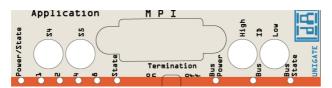


9 Hardware ports, switches and LEDs

9.1 Device labeling



Picture 1: Terminal labeling and termination



Picture 2: Front panel: Rotary switches, LEDs and termination MPI



In case the front panel should pop out it does not affect the device's function or quality. It can be put in again without problems.

9.2 Connectors

9.2.1 Connector to the external device (RS-interface)

The serial interface is available at the plug accessible on the upper side of the device.

Pin No.	Name	Function	
1	Rx 232	Receive signal	
2	Tx 232	Transmit signal	
3	AP-GND	Application Ground	
4	Rx 422+ (485+)	Receive signal	
5	Rx 422- (485-)	Receive signal	
6	Tx 422+ (485+)	Transmit signal	
7	Tx 422- (485-)	Transmit signal	

Pin assignment X1 (3-pole and 4-pole screw-type plug connector)



For the operation at a 485-interface the two pins labeled "485-" have to be connected together. Also the two pins "485+".

9.2.2 Connector supply voltage

Pin assignment X2 (4-pole screw-plug connector, on the bottom side, at the back)

Pin No.	Name	Function
1	UB (Pwr)	1033 V supply voltage / DC
2	0 V (Pwr)	0 V supply voltage / DC
3	n.c.	
4	n.c.	

9.2.3 MPI-bus-connector

The plug (labeled: MPI) for the connection to the MPI-bus is available on the front side of the device.

Pin assignment (9-pole D-SUB, socket)

Pin No.	Name	Function
1	Shield	
2		
3	В	Not inverting input-/output-signal from the MPI-bus
4	CNTR-P	Control signal / repeater
5	M5	DGND-data reference potential
6	P5	5 V supply voltage
7		
8	A	Inverting input-/output-signal from MPI-bus
9		

9.2.4 Power supply

The device must be powered with 10-33 VDC, The voltage supply is made through the 4-pole screw-plug connector at the device's bottom side.

Please note that the devices of the series UNIGATE should not be operated with AC voltage.

9.3 LEDs

The Gateway UNIGATE[®] CL - MPI features 9 LEDs with the following significance:

LED (Bus) Power	green	Supply voltage MPI-bus
LED Bus	green	MPI-bus-error
LED (Bus) State	red/green	Interface state Mpi-bus
LED Power	green	Supply voltage serial interface
LED State	red/green	Interface condition serial interface
LEDs 1 / 2 / 4 / 8 (Error No. / Select ID)	green	Binary display of the Error number

9.3.1 LED "(Bus) Power"

This LED is connected directly to the electrically isolated supply voltage of the MPI-bus-side.

9.3.2 LED "Bus"

This LED is directly controled by the MPI-bus ASIC and signals that the MPI-bus is in the state "DATA Sync.".

9.3.3 LED "(Bus) State"

Lights green	MPI-bus in the state data exchange "Data Sync."
Flashes green	Gateway waits for MPI-bus-configuration data or data exchange is interrupted
Flashes green/red	Bus is not initialized (Script command "Bus Start" not executed)
Lights red	General MPI-bus-error

9.3.4 LED "Power"

This LED is connected directly to the supply voltage of the serial interface (RS232/422/485).

9.3.5 LED "State"

Lights green	Data exchange active via serial interface
Flashes green	RS-interface OK, but no constant data exchange
Flashes green/red	No data exchange since switching on
Lights red	General Gateway error (see LEDs Error No.)
Flashes red	UNIGATE [®] is in the configuration / test mode

9.3.6 LEDs 1 / 2 / 4 / 8 (Error No. / Select ID)

If these 4 LEDs flash and LED "State" simultaneously lights red, the error number is displayed in binary notation (conversion table, see Annex) in accordance with the table in chapter "Error handling".

9.4 Switches

The Gateway features 7 switches with the following functions:

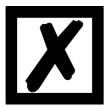
switchable Rx 422-terminating resistor for the serial interface
switchable Tx 422- or RS485-terminating resistor for the serial
interface
ID High for serial interface i. e. configmode
ID Low for serial interface i. e. configmode
switchable MPI-bus-terminating resistor
MPI-bus-ID (high byte)
MPI-bus-ID (low byte)

9.4.1 Termination Rx 422 + Tx 422 (serial interface)

If the Gateway is operated as the physically first or last device in an RS485-bus or as 422, there must be a bus termination at this Gateway. In order to do this the termination switch is set to position ON. The resistor (150 Ω) integrated in the Gateway is activated. In all other cases, the switch remains in position OFF.

Please refer to the general RS485 literature for further information on the subject of bus terminations.

If the integrated resistor is used, please allow for the fact that this also activates a pull-down resistor (390 Ω) to ground and a pull-up resistor (390 Ω) to VCC.



At RS48 only the Tx 422-switch must be set to ON. The Rx 422-switch has to be on OFF.

9.4.2 Rotary coding switches S4 + S5 (serial interface)

Via these two switches the RS485 -ID of the gateway is set to hexadecimal, as long as an ID is necessary for the bus. (A conversion table from decimal to hexadecimal can be found in the appendix.) This value is read in when the Gateway is switched on. The switch positions "EE" (testmode) and "FF" (config mode) are not possible for RS422- or RS485-operation.

Note: The switch position "DD" (ie, S4 and S5 in position "D") is reserved for internal purposes.

9.4.3 Termination (MPI)

If the Gateway is operated as the first or last physical device in the MPI-bus, there must be a bus termination at this Gateway. In order to do this, either a bus terminating resistor must be activated in the connector or the resistor (220 Ω) integrated in the Gateway must be activated. In order to do this, slide the slide switch to position ON. In all other cases, the slide switch must remain in position OFF.

Note: To activate or deactivate the bus termination, please remove the BUS-connector and carefully set the switch to the desired position.

9.4.4 Rotary coding switches High + Low (MPI-bus-ID)

With these two switches the Gateway's MPI-bus-ID (00... 1F) is set in hexadecimal notation. Please refer to the Annex for a conversion table from decimal to hexadecimal. This value is read in only once when the Gateway is activated.

10 Error handling

10.1 Error handling at UNIGATE[®] MB

If the Gateway detects an error, the error is signalled by the "State" LED lighting red and, simultaneously, the error number being indicated by means of LEDs "Error No." as shown in the table below. A distinction can be made between two error categories:

Serious errors (1-5): In this case, the Gateway must be switched off and switched back on again. If the error occurs again, the Gateway must be exchanged and returned for repair.

Warnings (6-15): These warnings are displayed for one minute simply for information purposes and are then automatically reset. If such warnings occur frequently, please inform After-Sales Service.

			1	Error no.	
LED8	LED4	LED2	LED1	resp. ID	Error description
0	0	0	0	0	Reserved
0	0	0	1	1	Hardware fault
0	0	1	0	2	EEROM error
0	0	1	1	3	Internal memory error
0	1	0	0	4	Fieldbus hardware error or wrong Fieldbus-ID
0	1	0	1	5	Script error
0	1	1	0	6	Reserved
0	1	1	1	7	RS-transmit buffer overflow
1	0	0	0	8	RS-receive buffer overflow
1	0	0	1	9	RS timeout
1	0	1	0	10	General fieldbus error
1	0	1	1	11	Parity-or frame-check-error
1	1	0	0	12	Reserved
1	1	0	1	13	Fieldbus configuration error
1	1	1	0	14	Fieldbus data buffer overflow
1	1	1	1	15	Reserved

In the configuration mode these displays are not valid and only meant for internal use.

Table 1: Error handling at UNIGATE[®] MB - system errors.

Flashing frequency 2 times per second (system error)

				Error-		
LED8	LED4	LED2	LED1	No.	Protocol	Error description
0	0	1	1	3	all Protocols	No Universal script support
0	1	0	1	5	all Protocols	Unknown Protocols
1	0	0	1	9	Modbus RTU Master	Timeout-Modbus Slave
					Modbus ASCII Master	Participant didn't response in set
						time frame (response time).
					Modbus RTU Slave	Timeout at Response-transmis-
					Universal	sion
					Modbus RTU Slave	
						Timeout - no response from Par-
					3964(R)	ticipant
1	0	1	1	11	Universal 232	Checksum of reception does not
					(with 232 Checksum)	match the calculated one.
					Modbus RTU Slave	Unknown error after response
					Universal	transmission.
					Modbus RTU Slave	
					3964(R)	Error at data exchange (e.g.
						Checksum error)
					Universal	Error in Response of Func-
1	1	0	0	12	Modbus RTU Master	tioncode
1	1	0	0	12	SSI	Error at the SSI communication
						General reception error at
					Modbus RTU Master	Modbus (ASCII) Exchange, e.g.
1	1	1	1	15	Modbus ASCII Master	Checksum error
1	1	1	0	14	Modbus RTU Slave	Exception Response
1	1	1	1	15		internal error at process data
					all Protocols	measurement

Table 2: Protocol based errors

Flashing frequency once per second (user-defined errors or protocol-related errors)

Note: The error is displayed as long as is defined with "Set Warning Time".

11 Installation guidelines

11.1 Installation of the module

The module with the dimensions $23 \times 117 \times 111 \text{ mm}$ (W x D x H) has been developed for switch cabinet use (IP 20) and can thus be mounted only on a standard mounting channel (deep DIN-rail to EN 50022).

11.1.1 Mounting

- Engage the module from the top in the top-hat rail and swivel it down so that the module engages in position.
- Other modules may be rowed up to the left and right of the module.
- There must be at least 5 cm clearance for heat dissipation above and below the module.
- The standard mounting channel must be connected to the equipotential bonding strip of the switch cabinet. The connection wire must feature a cross-section of at least 10 mm².

11.1.2 Removal

- First disconnect the power supply and signal lines.
- Then push the module up and swivel it out of the top-hat rail.

Vertical installation

The standard mounting channel may also be mounted vertically so that the module is mounted turned through 90°.

11.2 Wiring

11.2.1 Connection systems

The following connection systems must resp. may be used when wiring the module:

- Standard screw-type/plug connection (power supply + RS)
- 9-pin D-SUB plug connector (MPI-bus)
- a) In the case of standard screw-type terminals, one lead can be clamped per connection point. It is best to then use a screwdriver with a blade width of 3.5 mm to firmly tighten the screw.

Permitted cross-sections of the line:

- Flexible line with wire-end ferrule: 1 x 0.25 ... 1.5 mm²
- Solid conductor: 1 x 0.25 ... 1.5 mm²
- Tightening torque: 0.5 ... 0.8 Nm
- b) The plug-in connection terminal strip is a combination of standard screw-type terminal and plug connector. The plug connection section is coded and can thus not be plugged on the wrong way round.
- c) The 9-pin D-SUB plug connector is secured with two screws with "4-40-UNC" thread. It is best to use a screwdriver with a blade width of 3.5 mm to screw the screw tight. Tightening torque: 0.2... 0.4 Nm

11.2.1.1 Power supply

The device must be powered with 10..33 V DC.

• Connect the supply voltage to the 4-pole plug-in screw terminal in accordance with the labelling on the device.

11.2.1.2 Equipotential bonding connection

The connection to the potential equalization automatically takes place it is put on the DIN-rail.

11.2.2 MPI-bus communication interface

11.2.2.1 Bus line with copper cable

This interface is located on the module in the form of a 9-pin D-SUB socket on the front side of the housing.

• Plug the MPI-bus connector onto the D-SUB socket labelled "MPI-bus".

- Firmly screw the securing screws of the plug connector tight using a screwdriver.
- If the module is located at the start or end of the MPI-bus line, you must connect the bus terminating resistor integrated in the Gateway. In order to do this, slide the slide switch to the position labelled ...on...
- If the module is not located at the start or at the end, you must set the slide switch to position "off".

11.2.3 Line routing, shield and measures to combat interference voltage

This chapter deals with line routing in the case of bus, signal and power supply lines, with the aim of ensuring an EMC-compliant design of your system.

11.2.4 General information on line routing

- Inside and outside of cabinets

In order to achieve EMC-compliant routing of the lines, it is advisable to split the lines into the following line groups and to lay these groups separately.

\Rightarrow Group A:	• shielded bus and data lines (e. g. for MPI-bus, RS232C and printers etc.)
·	• shielded analogue lines
	• unshielded lines for DC voltages \geq 60 V
	• unshielded lines for AC voltage $\geq 25 \text{ V}$
	coaxial lines for monitors
\Rightarrow Group B:	• unshielded lines for DC voltages \geq 60 V and \geq 400 V
	• unshielded lines for AC voltage \geq 24 V and \geq 400 V
\Rightarrow Group C:	 unshielded lines for DC voltages > 400 V
The Ashis hale	

The table below allows you to read off the conditions for laying the line groups on the basis of the combination of the individual groups.

	Group A	Group B	Group C
Group A	1	2	3
Group B	2	1	3
Group C	3	3	1

Table 3: Line laying instructions as a function of the combination of line groups

- 1) Lines may be laid in common bunches or cable ducts.
- 2) Lines must be laid in separate bunches or cable ducts (without minimum clearance).
- 3) Lines must be laid in separate bunches or cable ducts inside cabinets but on separate cable racks with at least 10 cm clearance outside of cabinets but inside buildings.

11.2.4.1 Shielding of lines

Shielding is intended to weaken (attenuate) magnetic, electrical or electromagnetic interference fields.

Interference currents on cable shields are discharged to earth via the shielding bus which is connected conductively to the chassis or housing. A low-impedance connection to the PE wire is particularly important in order to prevent these interference currents themselves becoming an interference source.

Wherever possible, use only lines with braided shield. The coverage density of the shield should exceed 80%. Avoid lines with foil shield since the foil can be damaged very easily as the result of tensile and compressive stress on attachment. The consequence is a reduction in the shielding effect.

In general, you should always connect the shields of cables at both ends. The only way of achieving good interference suppression in the higher frequency band is by connecting the shields at both ends.

The shield may also be connected at one end only in exceptional cases. However, this then achieves only an attenuation of the lower frequencies. Connecting the shield at one end may be more favorable if

- it is not possible to lay an equipotential bonding line
- analogue signals (a few mV resp. mA) are to be transmitted
- foil shields (static shields) are used.

In the case of data lines for serial couplings, always use metallic or metallized plugs and connectors. Attach the shield of the data line to the plug or connector housing.

If there are potential differences between the earthing points, a compensating current may flow via the shield connected at both ends. In this case, you should lay an additional equipotential bonding line.

Please note the following points when shielding:

- Use metal cable clips to secure the shield braiding. The clips must surround the shield over a large area and must have good contact.
- Downstream of the entry point of the line into the cabinet, connect the shield to a shielding bus. Continue the shield as far as the module, but do not connect it again at this point!

12 Representation of the data in a S7

Any MPI-device (e. g. S7 - 300) can exchange data with the Gateway. It is also possible to use very "simple" master connections owing to the data structure.

12.1 Data exchange

After the SPS detects that a bus participant is ready for data exchange, data can be exchanged. The data in the input/output are determined by the Gateway. It has to fetch or retrieve the data using specific functional modules.

13 Technical data

13.1 Device data

The technical data of the module is given in the table below.

No.	Parameter	Data	Explanations
1	Location	Switch cabinet	DIN-rail mounting
2	Enclosure	IP20	Protection against foreign bodies and water to IEC 529 (DIN 40050)
4	Service life	10 years	
5	Housing size	23 x 117 x 111 mm (screw-plug-connector included) 23 x 117 x 100 mm (screw-plug connector not included)	WxDxH
6	Installation position	Any	
7	Weight	123 g	
8	Operating temperature	-40°C +85°C	
9	Storage/transport temperature	-40 °C +85 °C	
10	Atmospheric pressure during operation during transport	795 hPa 1080 hPa 660 hPa 1080 hPa	
11	Installation altitude	2000 m 4000 m	Unrestricted Restricted - Ambient temperature ≤ 40°C
12	Relative humidity	Max. 80 %	No condensation, no corrosive atmosphere
14	External power supply	1033 V DC	Standard power supply unit to DIN 19240
15	Current consumption at 24 VDC	Typ. 120 mA max 150 mA	At 10.8V. typ. 350 mA
16	Supply at the MPI-bus-inter- face	5 V DC / max. 50 mA	(Max. 50 mA at < 30°C ambient temperature)
17	Reverse voltage protection	Yes	But does not function!
18	Short-circuit protection	Yes	
19	Overload protection	Poly-switch	Thermal fuse
20	Undervoltage detection (USP)	≤9 V DC	
21	Emergency power supply	≥ 5 ms	Device fully operable

Table: Technical data of the module

13.1.1 Interface data

The table below lists the technical data of the interfaces and ports on the device. The data has been taken from the corresponding Standards.

	Interface designation	MPI-bus	RS232-C	RS485/RS422
No.	Physical interface	RS485	RS232-C	RS485/RS422
1	Standard	EIA Standard	DIN 66020	EIA Standard
2	Transmission mode	Symmetrical	Asymmetrical	Symmetrical
		asynchronous	asynchronous	asynchronous
		serial	serial	serial
		half-duplex	full duplex	half-duplex
		•	•	full duplex at RS422
		\rightarrow Difference signal	\rightarrow Level	\rightarrow Difference signal
3	Transmission method	Master / master	Master / slave	Master / slave
4	Number of users :			
	- Transmitters	32	1	32
	- Receiver	32	1	32
5	Cable length:		15 m	1200 m
	 Baud rate-dependent 	187.5 kBd \rightarrow 1000 m	no	$< 93.75 \text{ kBd} \rightarrow 1200 \text{ m}$
				312, kBd \rightarrow 500 m
				$625 \text{ kBd} \rightarrow 250 \text{ m}$
6	Bus topology	Line	Point-to-point	Line
7	Data rate: - Maximum	12 Mbit/s	120 kBit/s	625 kBaud
			0.41/5	
	- standard values	9.6 kBit/s	2.4 k/B	2.4 kBit/s
		19.2 kBit/s	4.8 k/B	4.8 kBit/s
		93.75 kBit/s	9.6 kBit/s	9.6 kBit/s
		187.5 kBit/s \rightarrow standard	19.2 kBit/s	19.2 kBit/s
		500 kBit/s	38.4 kBit/s	57.6 kB
		1.5 Mbit/s		312.5 kB
		3 MBit/s		625 kB
		6 MBit/s		
8	Transmitter: - Load	12 Mbit/s 54 Ω	3 7 kΩ	54 Ω
0	- Maximum voltage	- 7 V 12 V	± 25 V	- 7 V 12 V
	- Signal, unloaded	± 5 V	± 15 V	± 5 V
	- Signal, loaded	± 1.5 V	± 5 V	± 1.5 V
9	Receiver: - Input resistance	12 Ω	<u>-</u> 3 ν 3 7 Ω	12 Ω
5	- Max. input signal	- 7 V 12 V	± 15 V	- 7 V 12 V
	- Sensitivity	± 0.2 V	± 3 V	± 0.2 V
10	Transmit range (SPACE):	- 0.2 + 0.2 V	+ 3 + 15 V	- 0.2 + 0.2 V
	- Voltage level	0	0	0
	- Logic level	-	-	-
11	Transmit pause (MARK):	+ 1.5 +5 V	- 3 –15 V	+ 1.5 +5 V
.	- Voltage level	1	1	1
	- Logic level			
L	2091010101			

Table: Technical data of the interfaces at the module

14 Commissioning guide

14.1 Note

Only trained personnel following the safety regulations may commission the UNIGATE[®].

14.2 Components

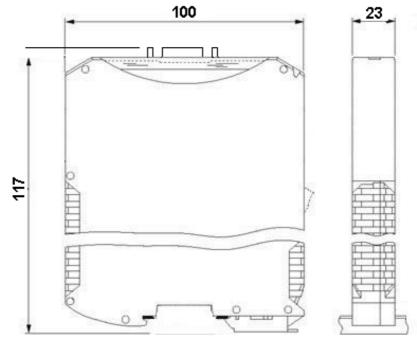
You will require the following components to commission the UNIGATE®:

- UNIGATE[®]
- Connection cable from Gateway to the process
- Connector for MPI-bus connection to the Gateway
- MPI-bus-cable cable (this cable is generally already installed on site!)
- 10..33 V DC power supply (DIN 19240)
- The instruction manual can be downloaded free of charge from our website at www.deutschmann.de.

14.3 Installation

The UNIGATE[®] MB - MPI module features protection type IP20 and is thus suitable for switch cabinet use. The device is designed for snapping onto a 35 mm DIN-rail.

14.4 Dimensional drawing UNIGATE® MB - MPI



14.5 Commissioning

It is essential that you perform the following steps during commissioning in order to ensure that the module operates correctly:

14.6 Setting the MPI-address

Set the MPI-address at the fieldbus end of the module on the two rotary switches designated "High" and "Low". This adjustment is carried out in a hexadecimal way. Example:

The MPI-bus-ID is 26 decimal = 1A hexadecimal

The switch "High" has to be set to 1 and the switch "Low" has to be set to A.



Attention:

The MPI-bus must contain the set MPI-bus-address only once! It is read in only on power-up of the Gateway!

14.7 MPI-bus-connection

Connect the device to the MPI-bus at the interface labelled "MPI".

14.8 Connection to the process device

Please also read the manual for the process device when commissioning the process device.

14.9 Connecting the supply voltage

Please connect 10..33 DC voltage to the terminals provided for this.

14.10 Shield connection

Earth the top-hat rail onto which the module has been snapped.

14.11 Literature

In order to help you quickly get to grips with the subject we recommend the book "Crashkurs S7 mit CD". The book can be ordered from the VDE Verlag (ISBN: 3-8007-2377-8).

15 Servicing

Should questions arise that are not covered in this manual you can find further information in our

• FAQ/Wiki area on our homepage www.deutschmann.com or directly in our Wiki on www.wiki.deutschmann.de

If your questions are still unanswered please contact us directly.

Please note down the following information before calling:

- Device designation
- Serial number (S/N)
- Article number
- Error number and error description

Your request will be recorded in the Support center and will be processed by our Support Team as quickly as possible (Usually in 1 working day, rarely more than 3 working days.).

Technical Support hours are as follows: Monday to Thursday from 8 am to midday and from 1 pm to 4 pm, Friday from 8 am to midday (CET).

Deutschmann Automation GmbH & Co. KG Carl-Zeiss-Straße 8 D-65520 Bad-Camberg Germany

Central office and sales department	+49 6434 9433-0
Technical Support	+49 6434 9433-33
Fax sales department	+49 6434 9433-40
Fax Technical Support	+49 6434 9433-44
E-mail Technical Support	support@deutschmann.de

15.1 Returning a device

If you return a device, we require as comprehensive a fault/error description as possible. We require the following information in particular:

- What error number was displayed?
- What is the supply voltage (±0.5 V) with Gateway connected?
- What were you last doing or what last happened on the device (programming, error on power-up, ...)?

The more precise information a fault/error description you provide, the more exactly we will be able to pinpoint the possible causes.

15.2 Downloading PC software

You can download current information and software free of charge from our Internet server. http://www.deutschmann.com.

16 Annex

16.1 Explanations of the abbreviations

General	-	
CL	=	Product group CL (Compact Line)
CM	=	Product group CM (CANopen Line)
CX	=	Product group CX
EL	=	Product group EL (Ethernet Line)
FC	=	
		Product group FC (Fast Connect)
GT	=	Galvanic separation RS-side
GY	=	Housing color gray
MB	=	Product group MB
RS	=	Product group RS
SC	=	Product group SC (Script)
232/485	=	Interface RS232 and RS485 switchable
232/422	=	Interface RS232 and RS422 switchable
DB	=	Additional RS232 DEBUG-interface
D9	=	Connection of the RS through 9-pin D-SUB instead of 5-pin screw-plug connector
PL	=	Board only without DIN-rail module and without housing cover
PD	=	Board only without DIN-rail module and with housing cover
AG	=	Gateway installed in a die-cast aluminum housing
EG	=	Gateway installed in a stainless steel housing
IC2	_	
IC2 IC	=	Product group IC2 (IC-design DIL32)
108	=	Product group IC (IC-design DIL32)
108	=	Option I/O8 Script memory expanded to 16KP
5V	=	Script memory expanded to 16KB
	=	- F - · · · · · · · · · · · · · · · · ·
3,.3V	-	Operating voltage 3.3V
Fieldbus	;	
CO	=	CANopen
C4	=	CANopen V4
C4X	=	CANopen V4-version X (see comparison table UNIGATE [®] IC for the respective
0477		product)
DN	=	DeviceNet
EC	=	EtherCAT
EI	=	
FE	=	
FEX	=	
I LX		respective product)
IB	=	Interbus
IBL	=	Interbus
LN62	=	LONWorks62
LN02 LN512	_	LONWorks512
ModTCP		ModbusTCP
MPI	=	Siemens MPI [®]
PL	=	Powerlink
PN	=	Profinet-IO

PBDP = ProfibusDP

PBDPL =	=	ProfibusDP-version L (see comparison table UNIGATE [®] IC for the respective product)
PBDPX =	=	ProfibusDP-version X (see comparison table UNIGATE [®] IC for the respective product)
PBDPV0 =	=	ProfibusDPV0
PBDPV1 =	=	ProfibusDPV1
RS =	=	Serial RS232/485/422

16.2 Hexadecimal table

Hex	Decimal	Binary
0	0	0000
1	1	0001
2	2	0010
3	3	0011
4	4	0100
5	5	0101
6	6	0110
7	7	0111
8	8	1000
9	9	1001
А	10	1010
В	11	1011
С	12	1100
D	13	1101
E	14	1110
F	15	1111

62